



FIG. 1

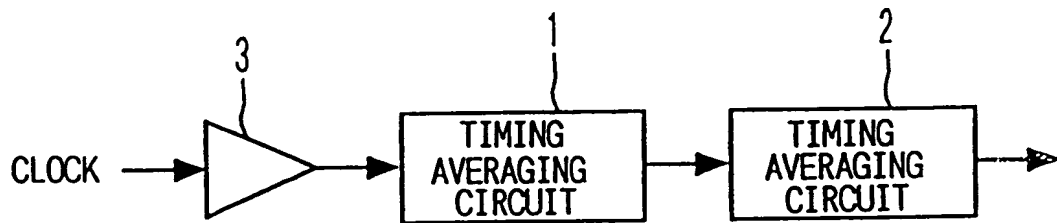


FIG. 2

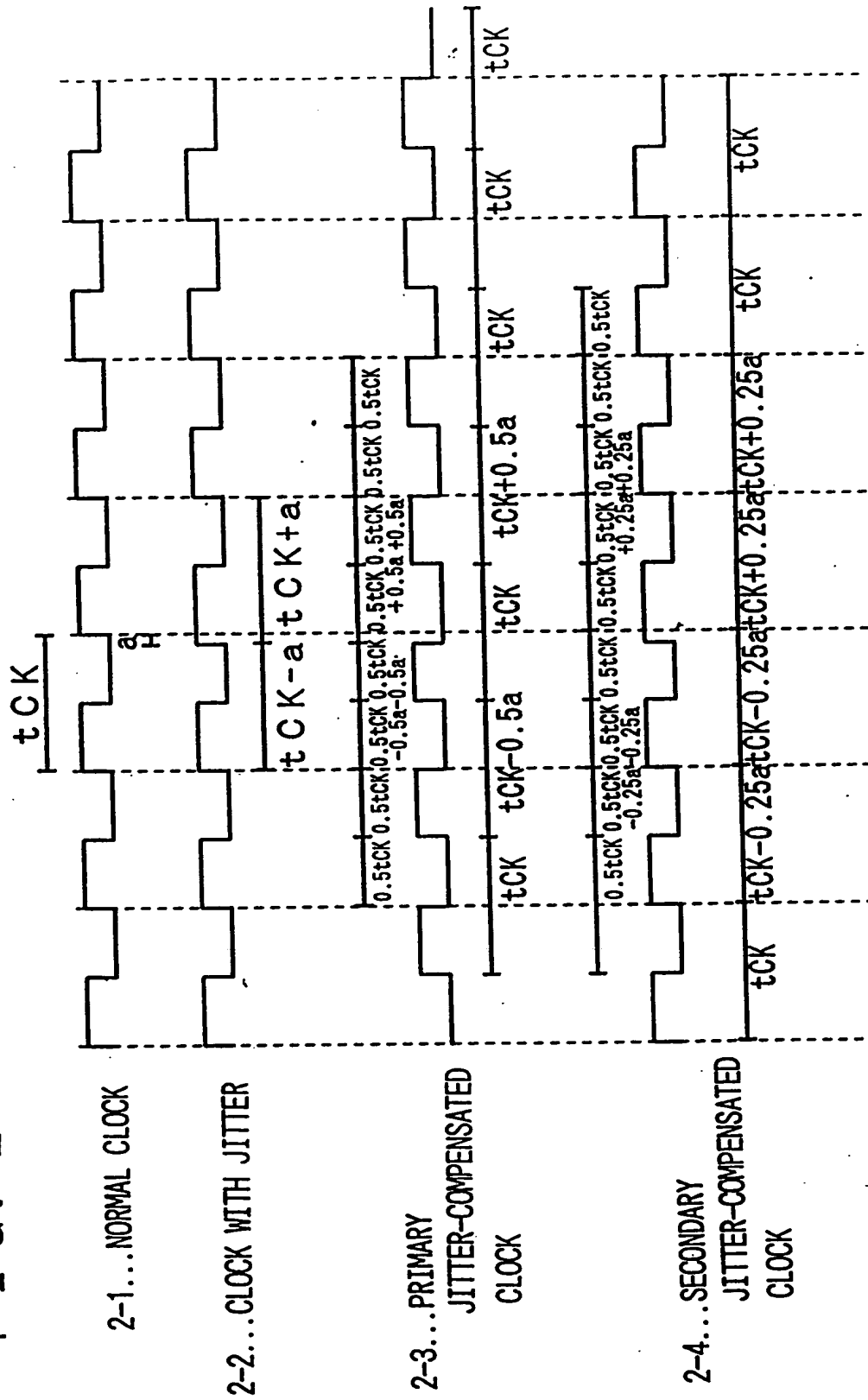


FIG. 3

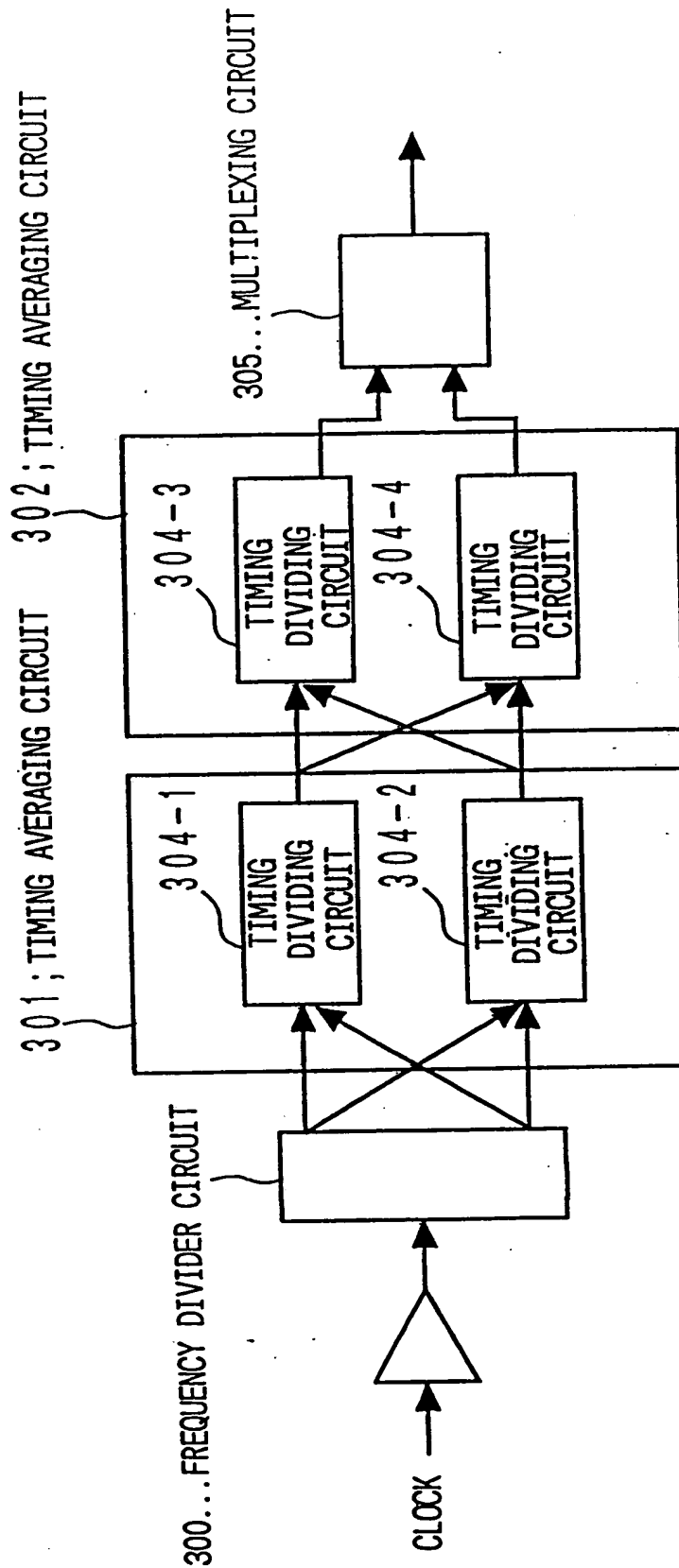


FIG. 4 (a)

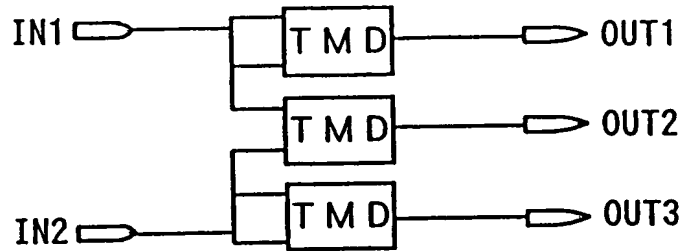


FIG. 4 (b)

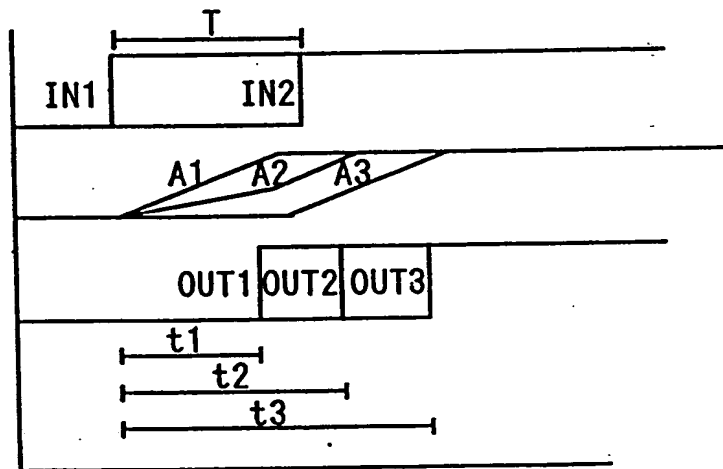


FIG. 4 (c)

$$t1 = CV / (i1 + i2)$$

$$t2 = T + (CV - i1T) / (i1 + i2) = T + CV / (i1 + i2) - i1T / (i1 + i2) = T(i2 / (i1 + i2)) + t1$$

$$t2 = (1/2)T + t1$$

$$t3 = T + CV / (i1 + i2) = T + t1$$

FIG. 5 (a)

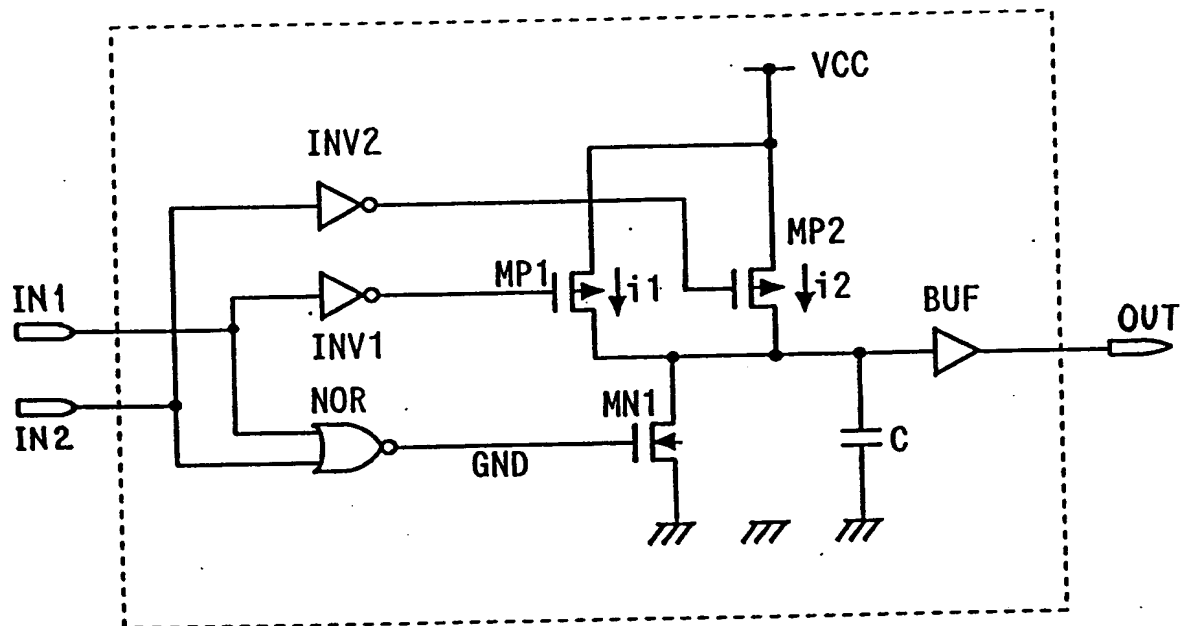


FIG. 5 (b)

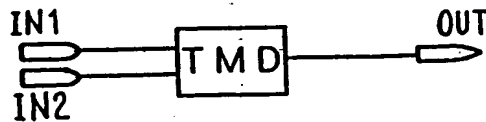


FIG. 6

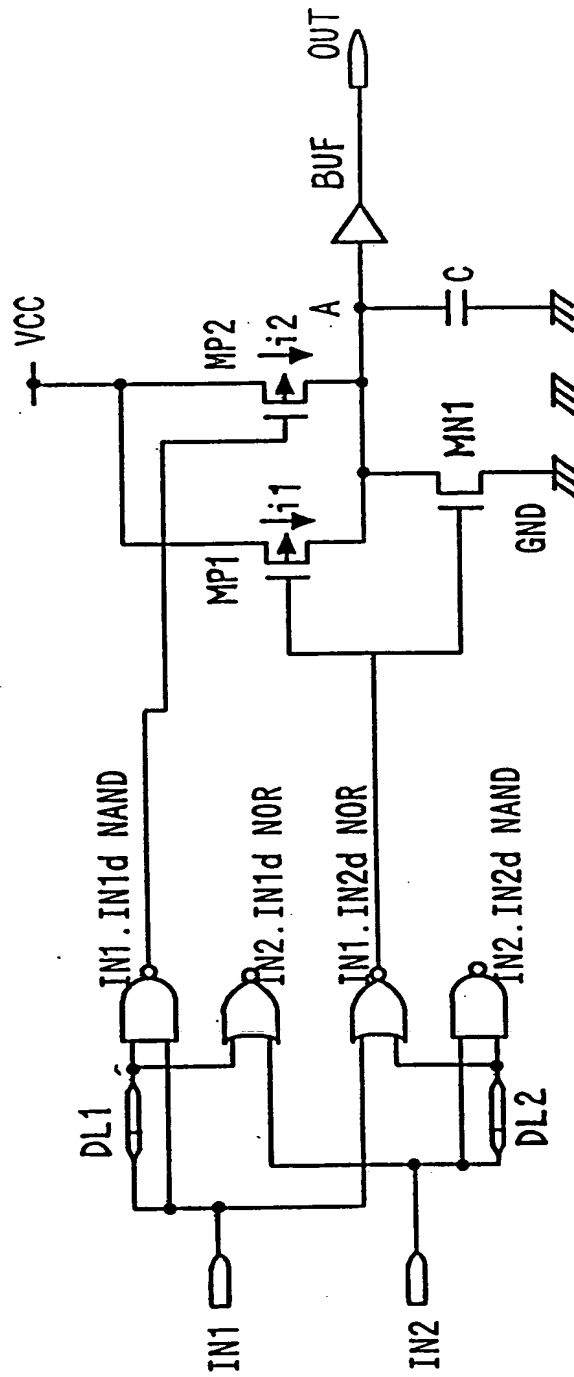


FIG. 7

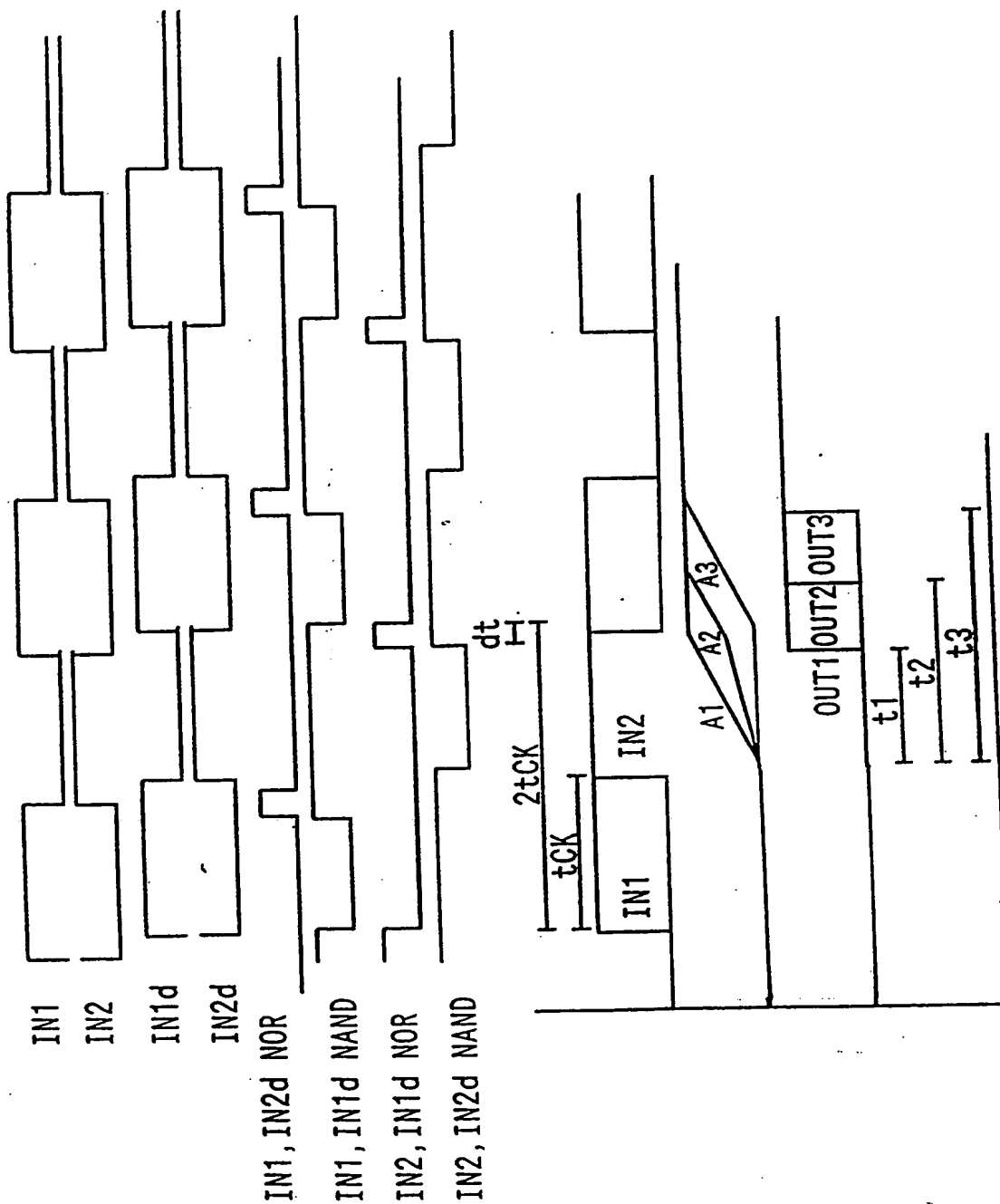


FIG. 8

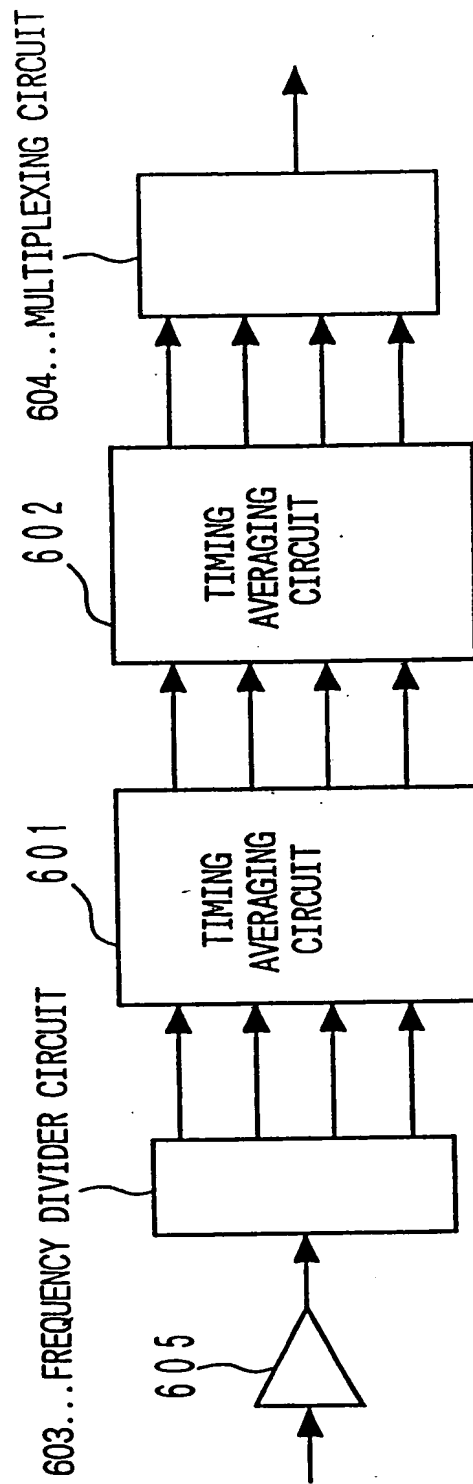


FIG. 9

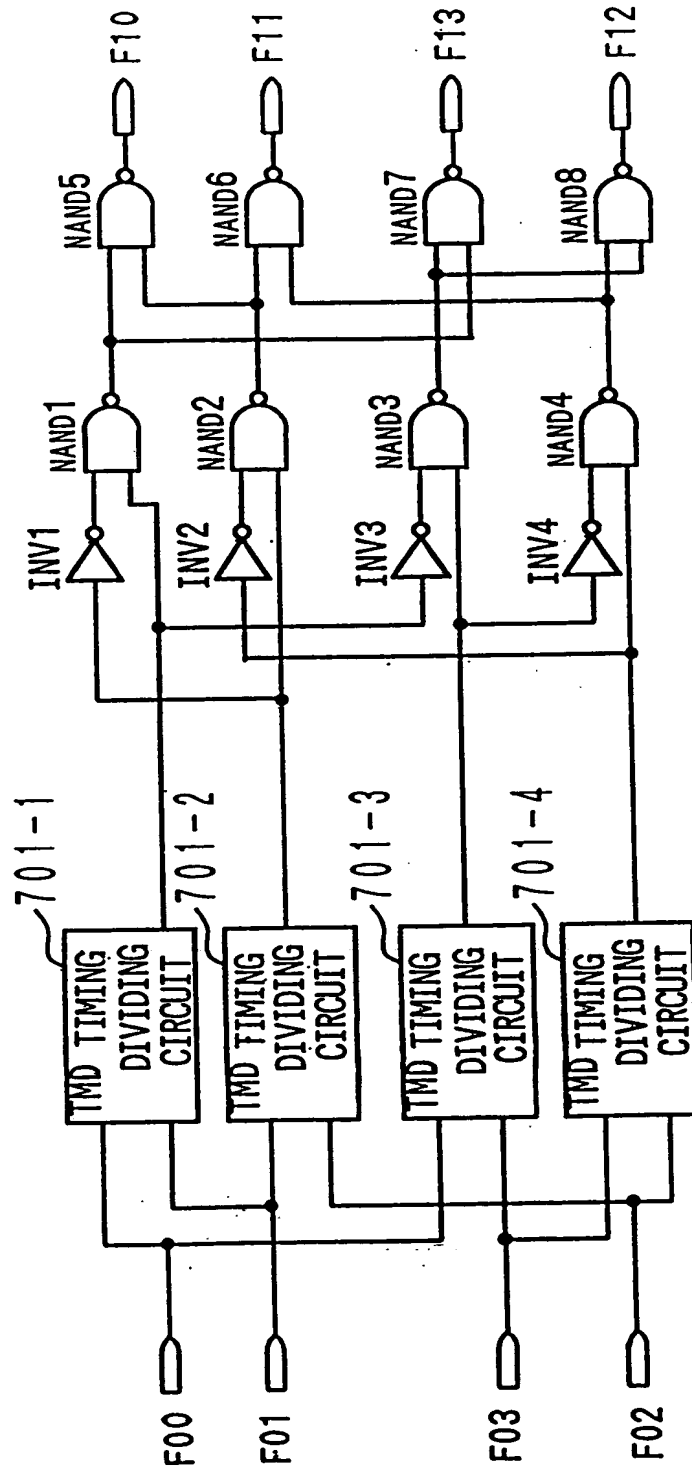


FIG. 10

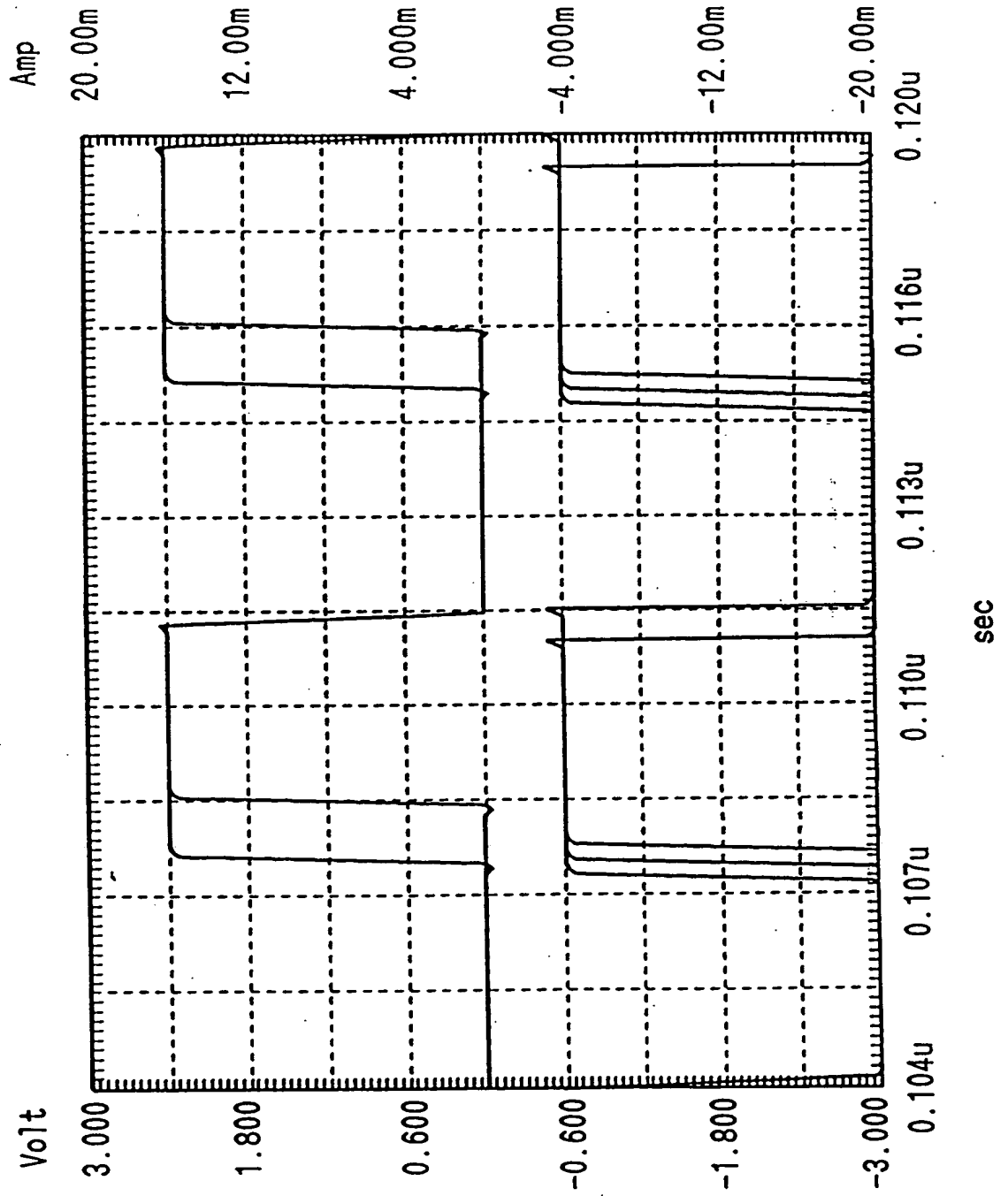


FIG. 11

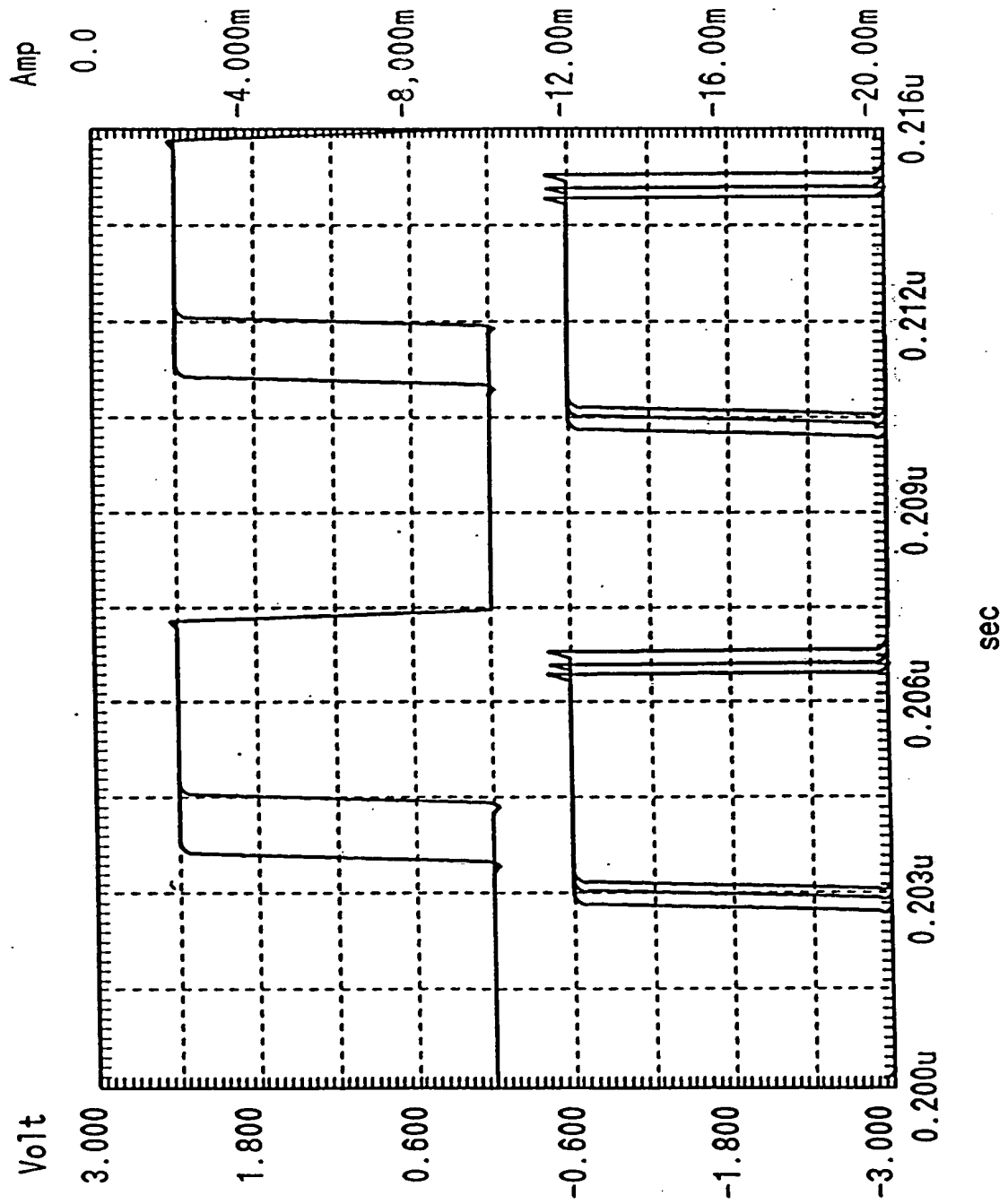


FIG. 12

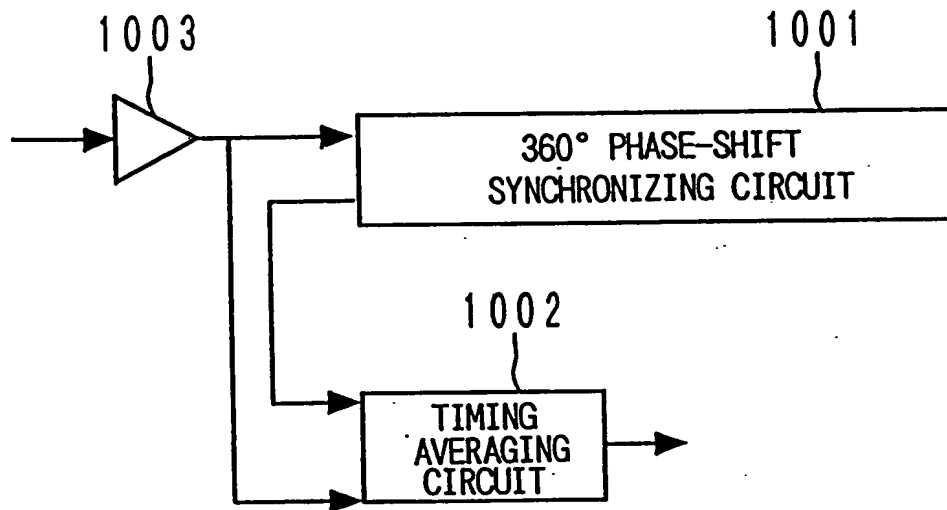


FIG. 13

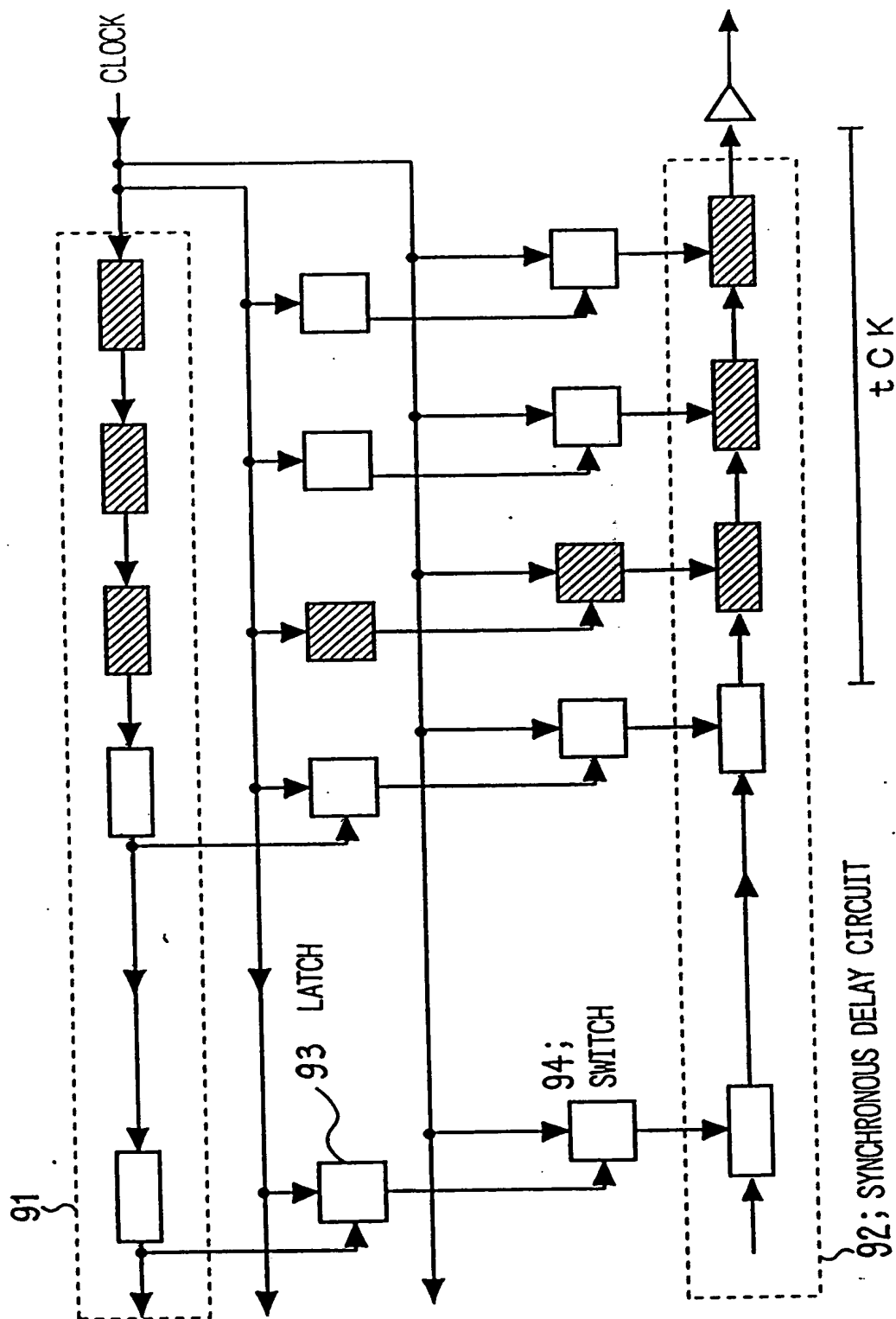


FIG. 14

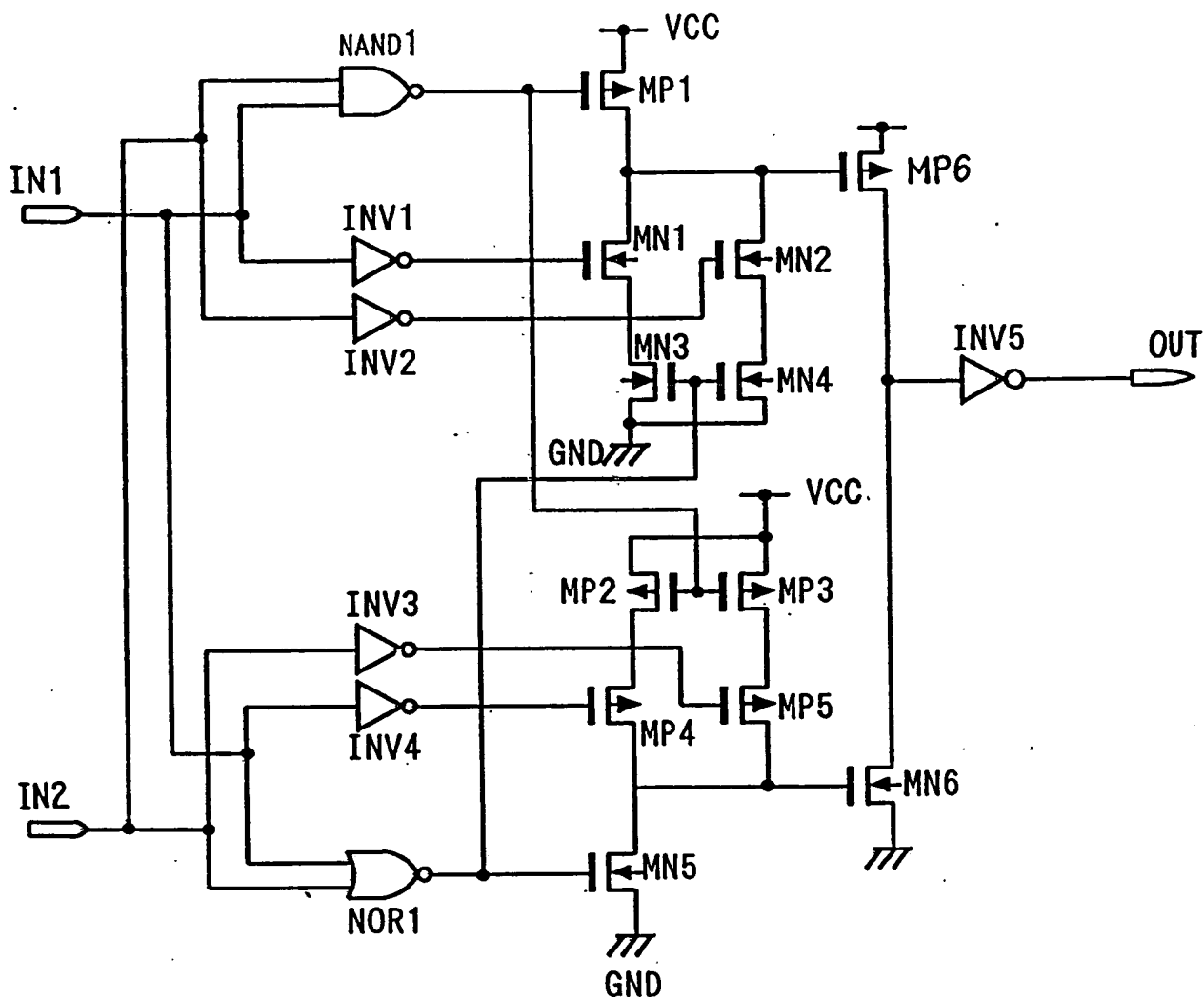


FIG. 15

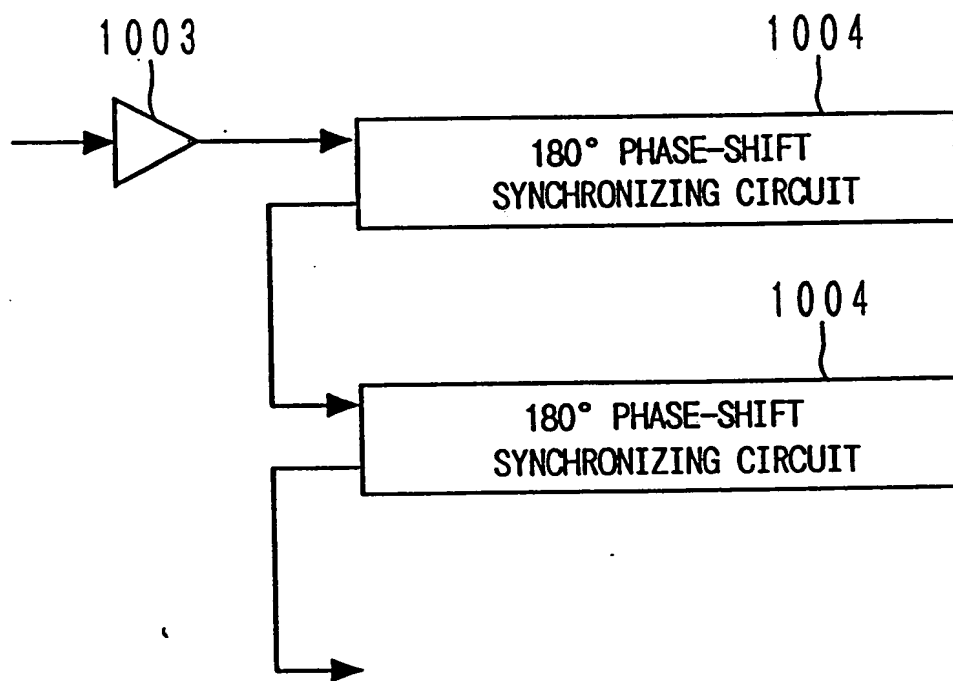


FIG. 16

PRIOR ART

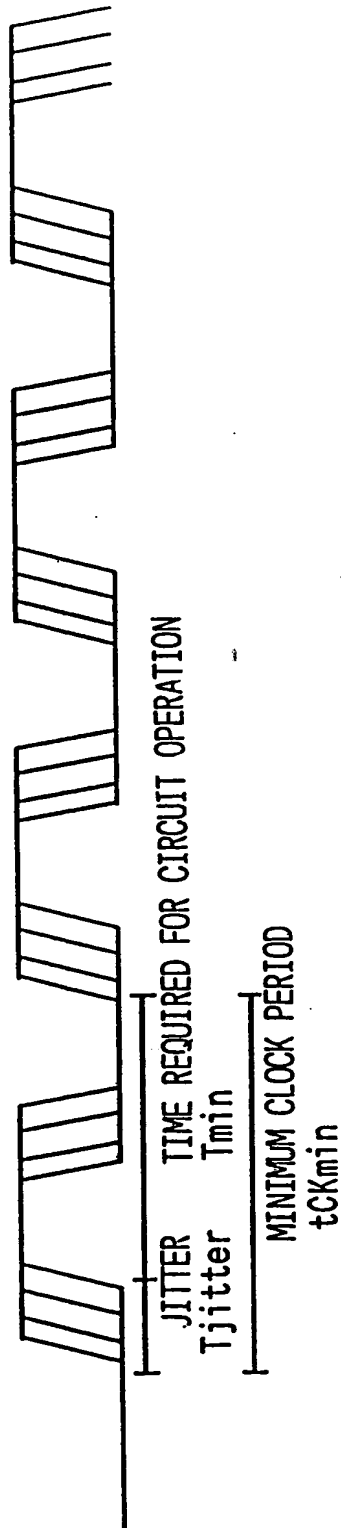
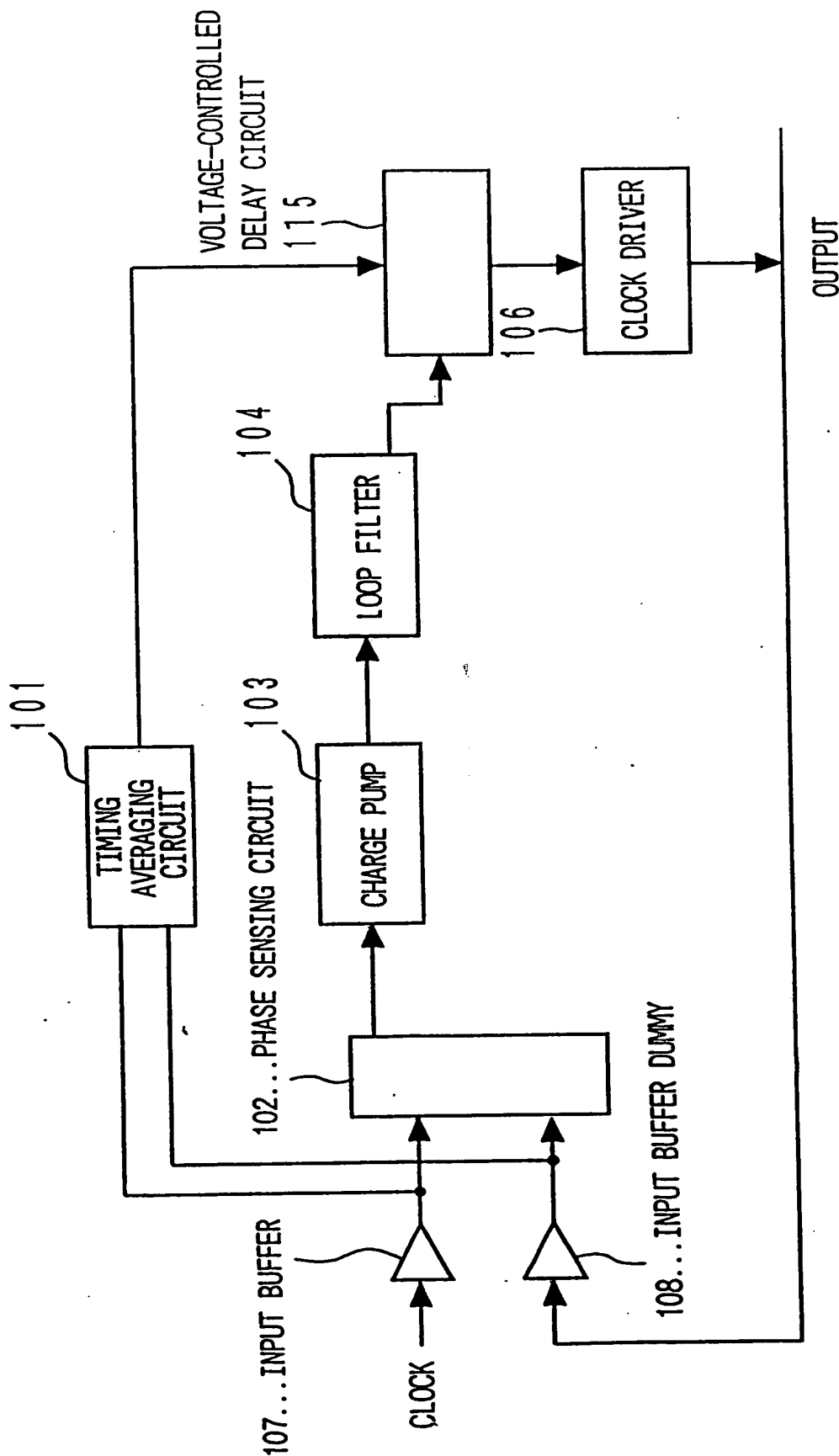


FIG. 17



2-1...NORMAL CLOCK

2-2...CLOCK WITH JITTER

2-3...COMPENSATED CLOCK

The diagram shows three clock signals over time. The first signal is a normal clock with period t_{CK} . The second signal is a clock with jitter, where the period is $t_{CK} - a$. The third signal is a compensated clock, where the period is t_{CK} . The jitter is represented by a horizontal line segment labeled a . The compensated clock period is shown as $t_{CK} - 0.5a$ and $t_{CK} + 0.5a$.

FIG. 19

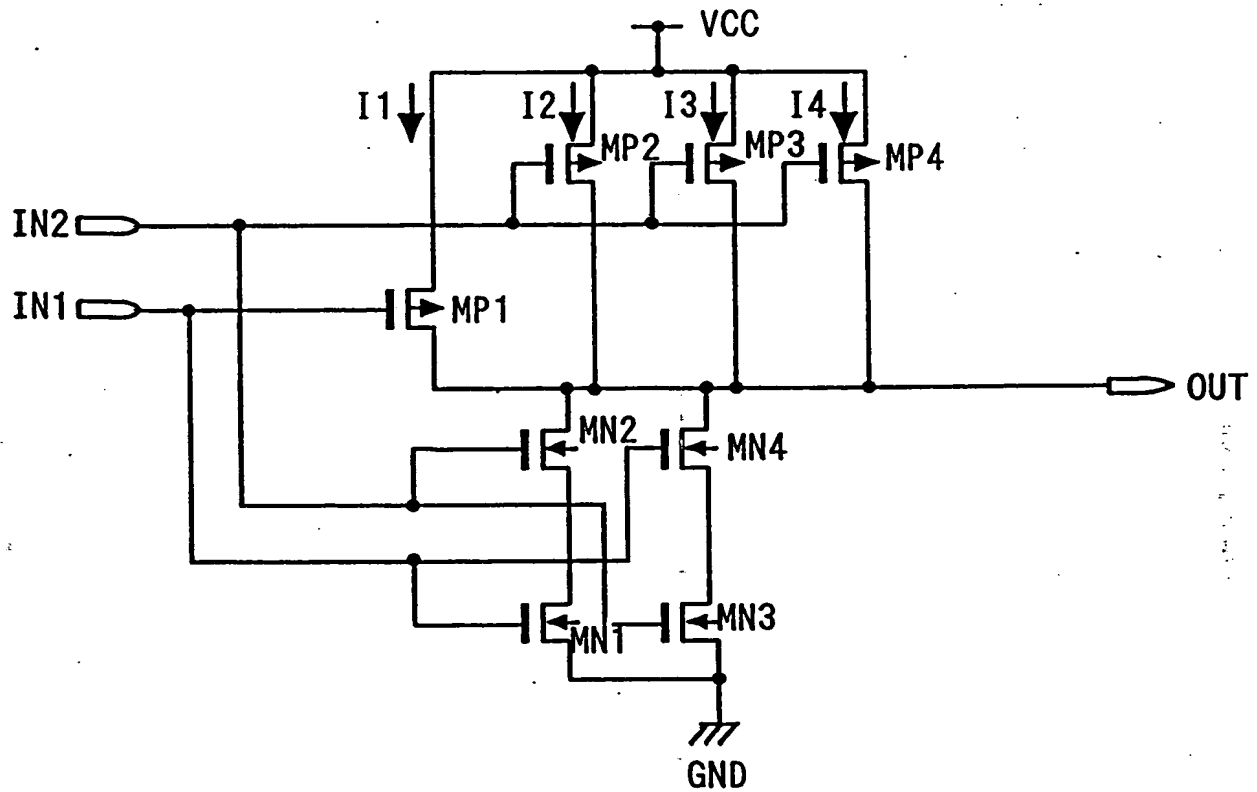


FIG. 20

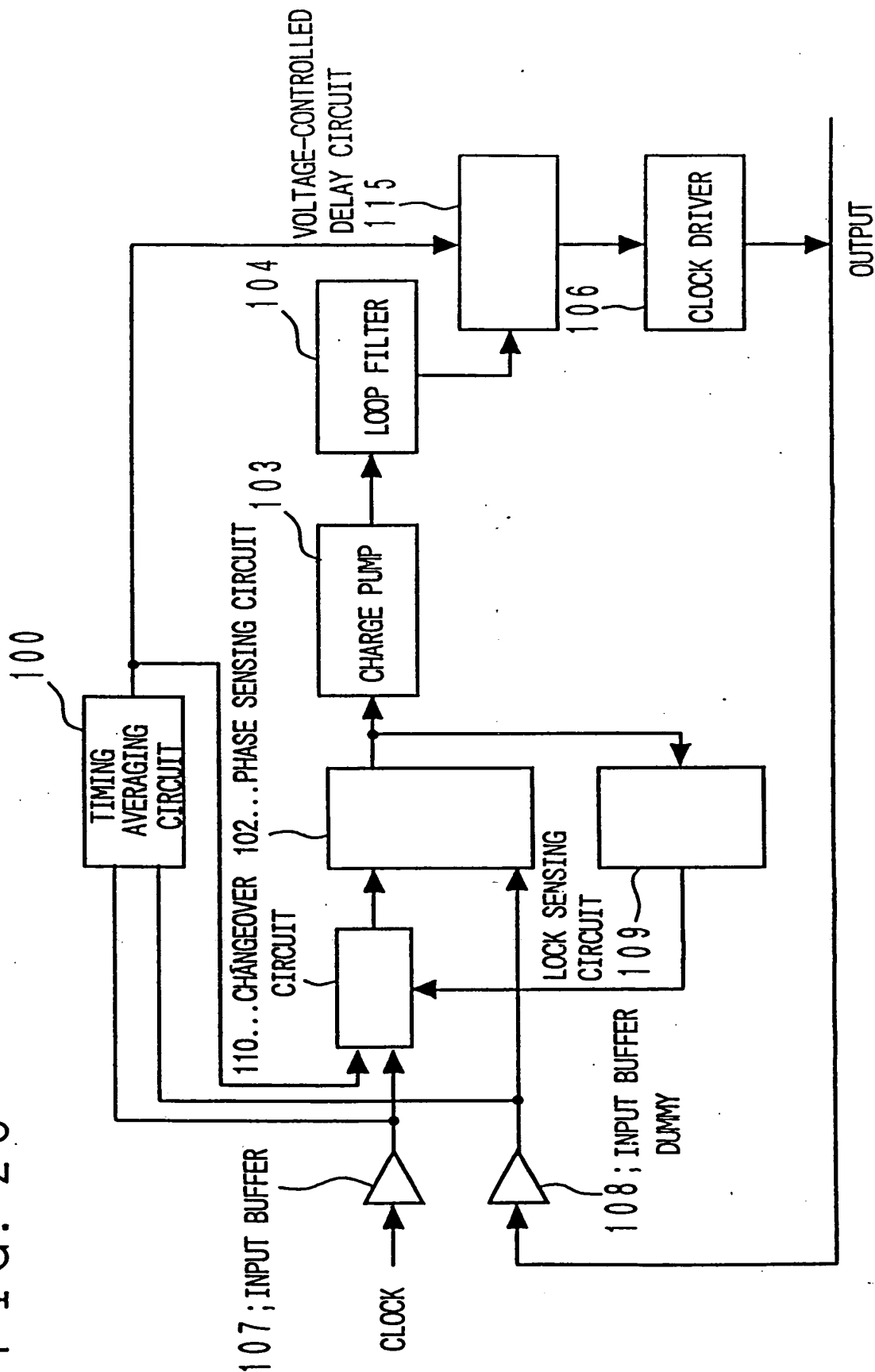
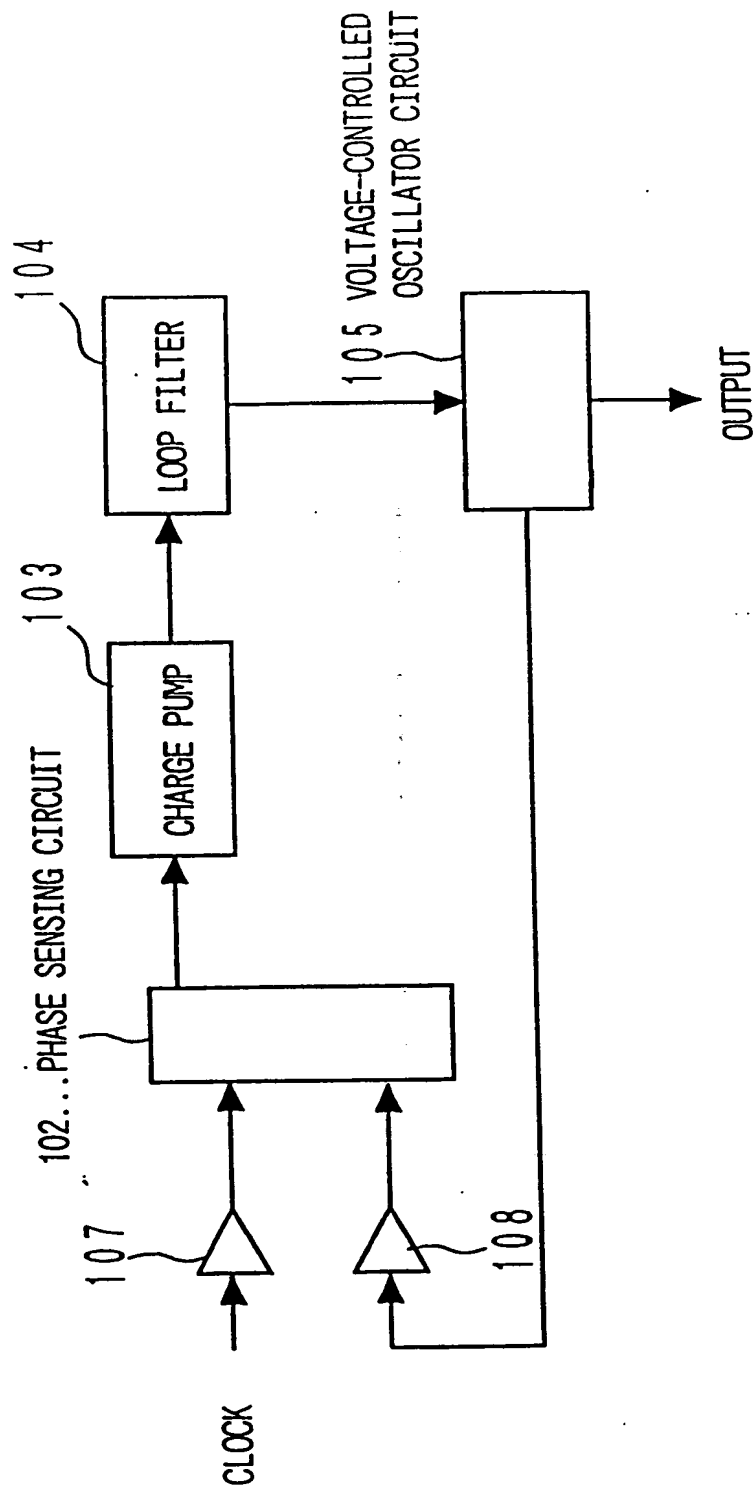


FIG. 21

RELATED ART



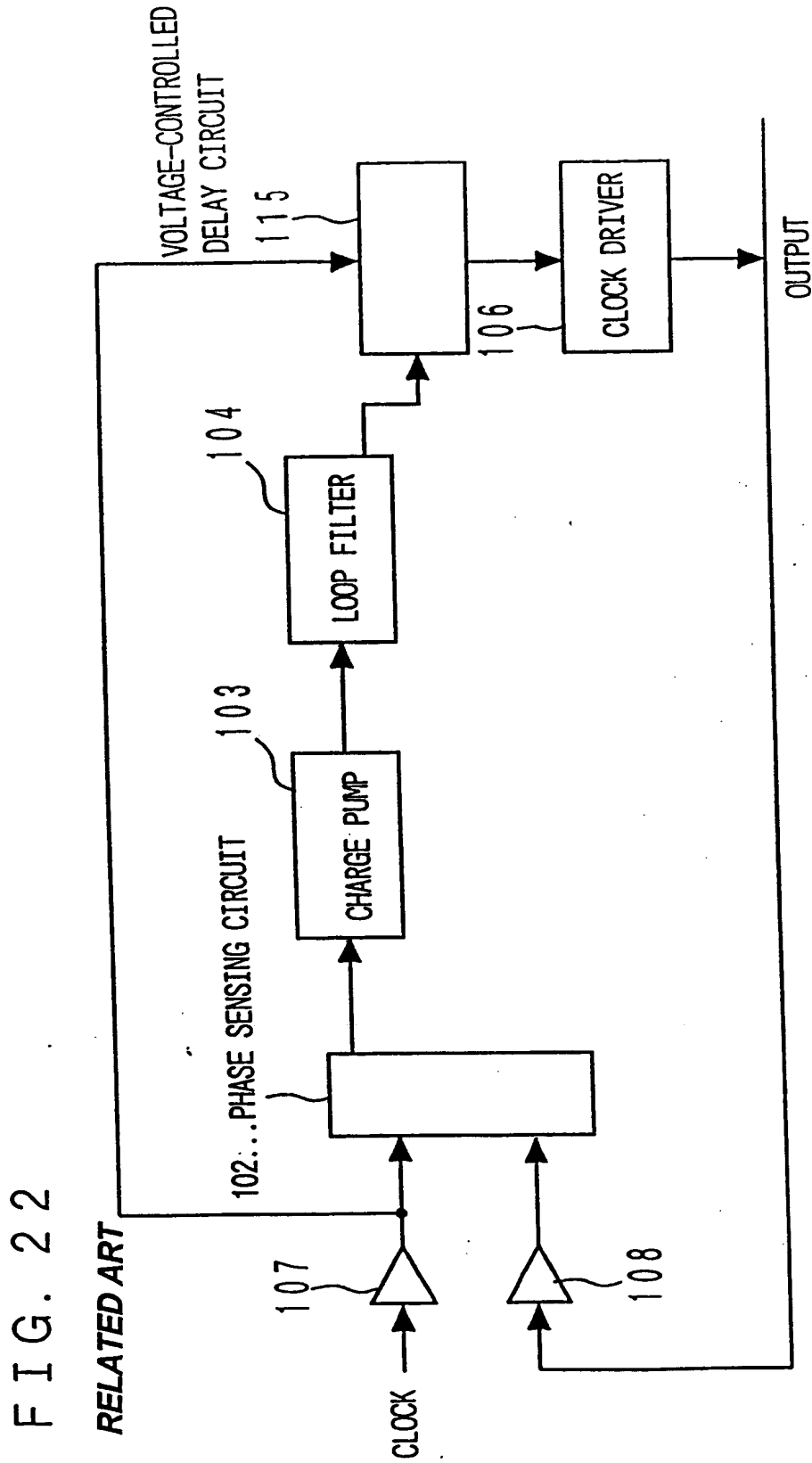


FIG. 23 (a)

RELATED ART

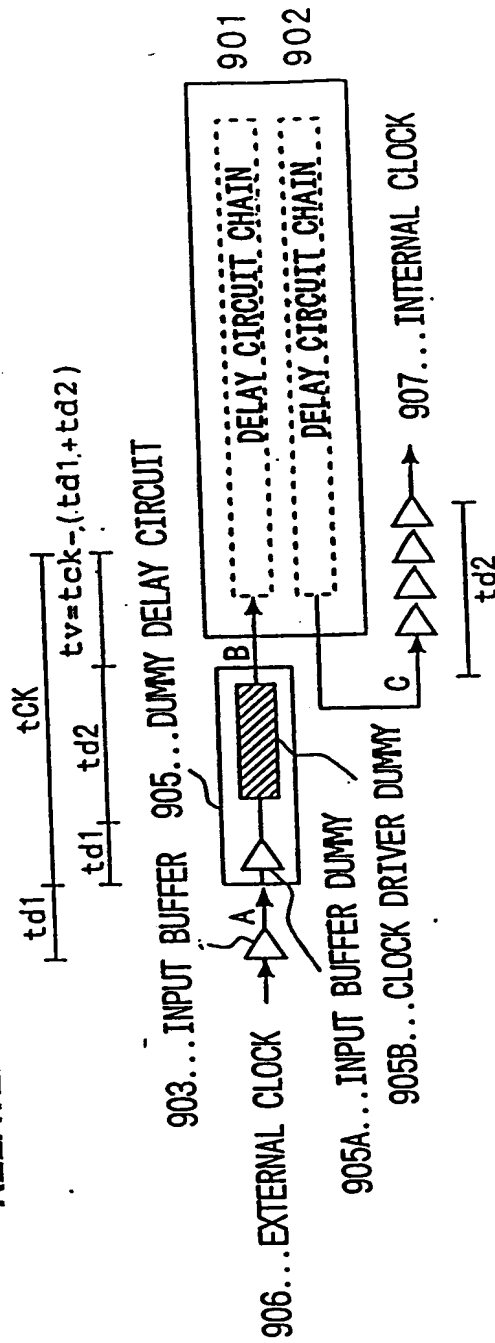


FIG. 23 (b)

RELATED ART

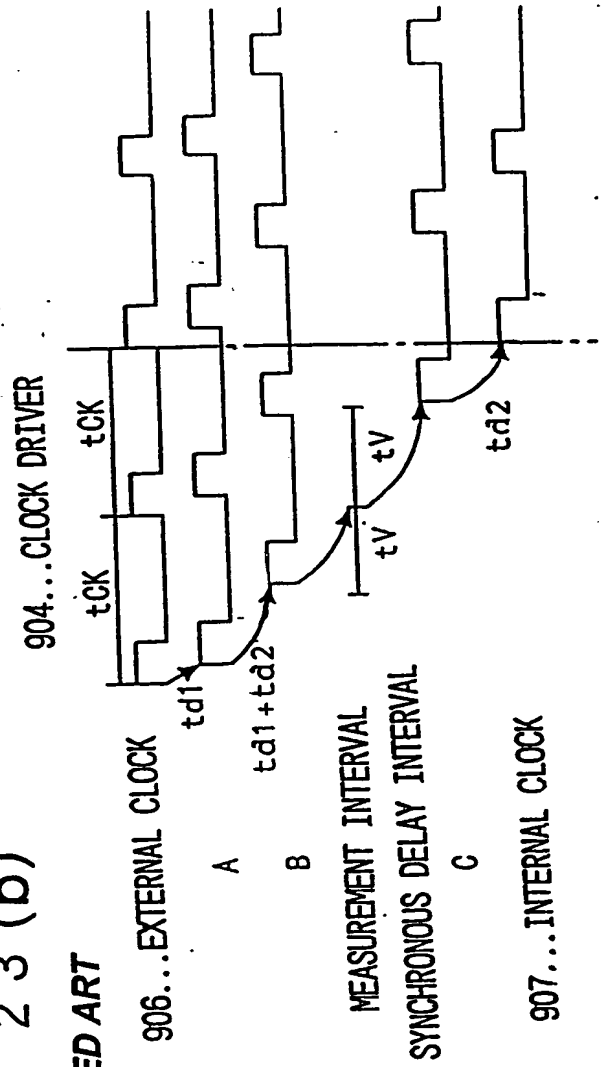


FIG. 24
RELATED ART

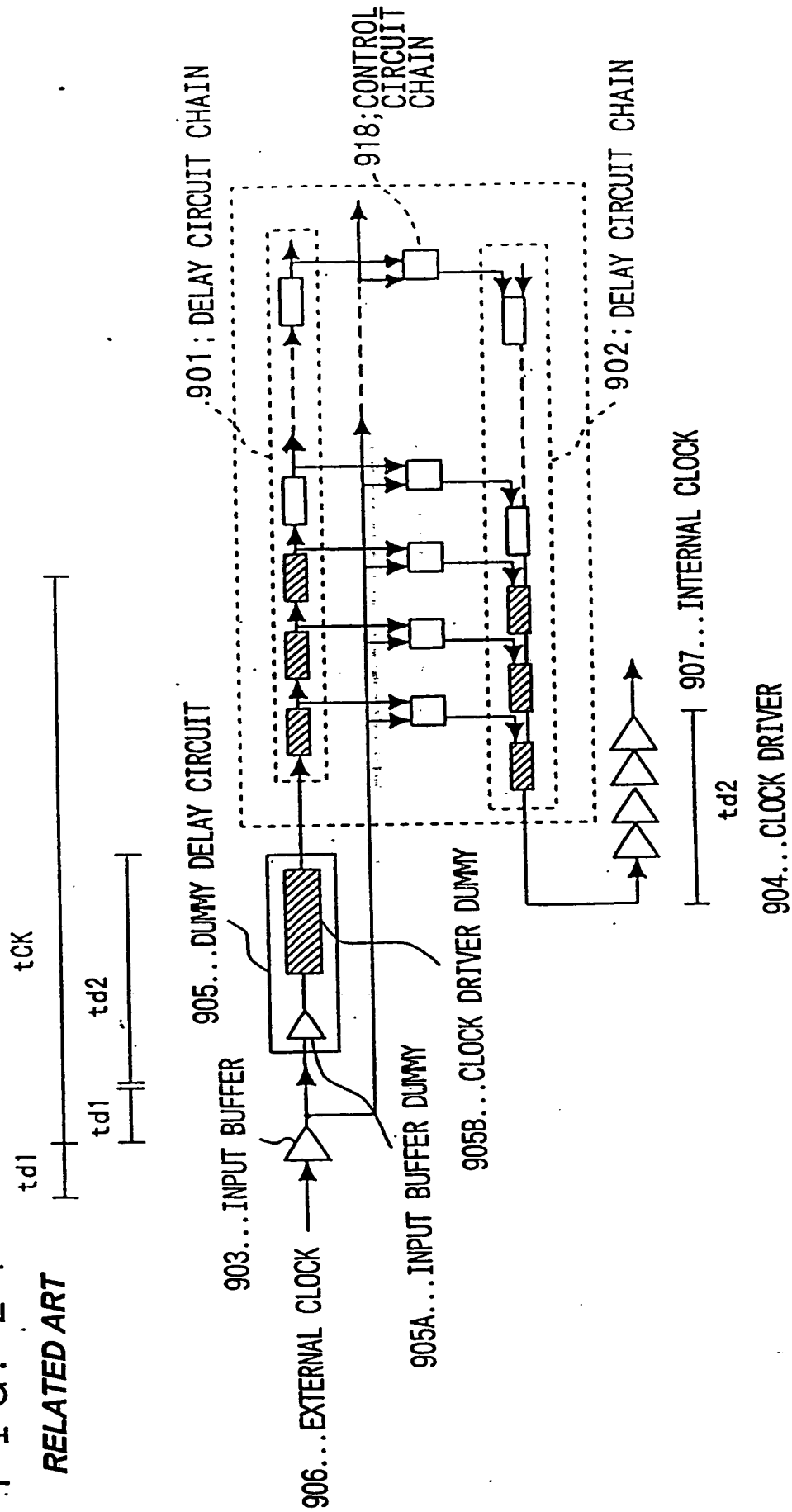


FIG. 25

RELATED ART

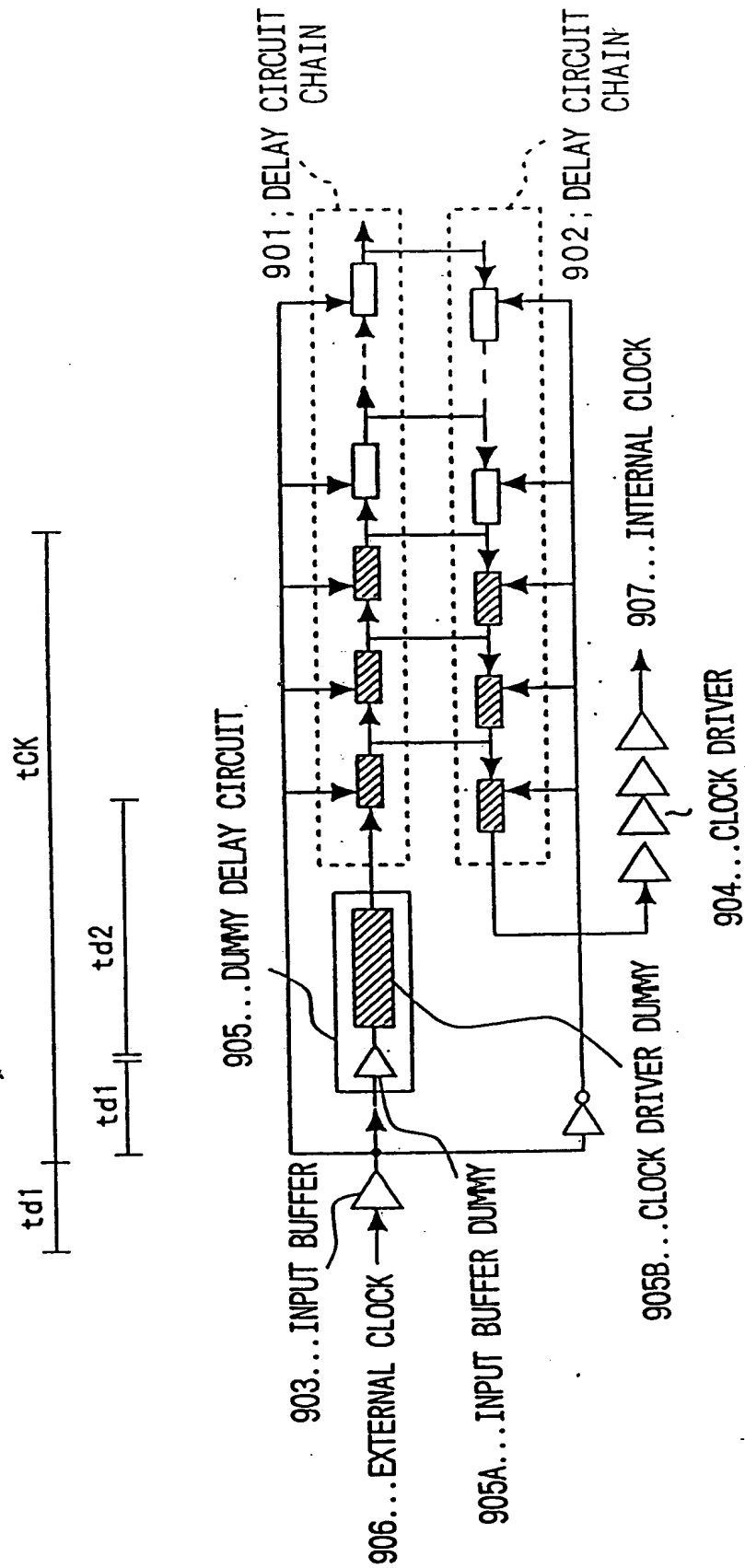


FIG. 26
RELATED ART

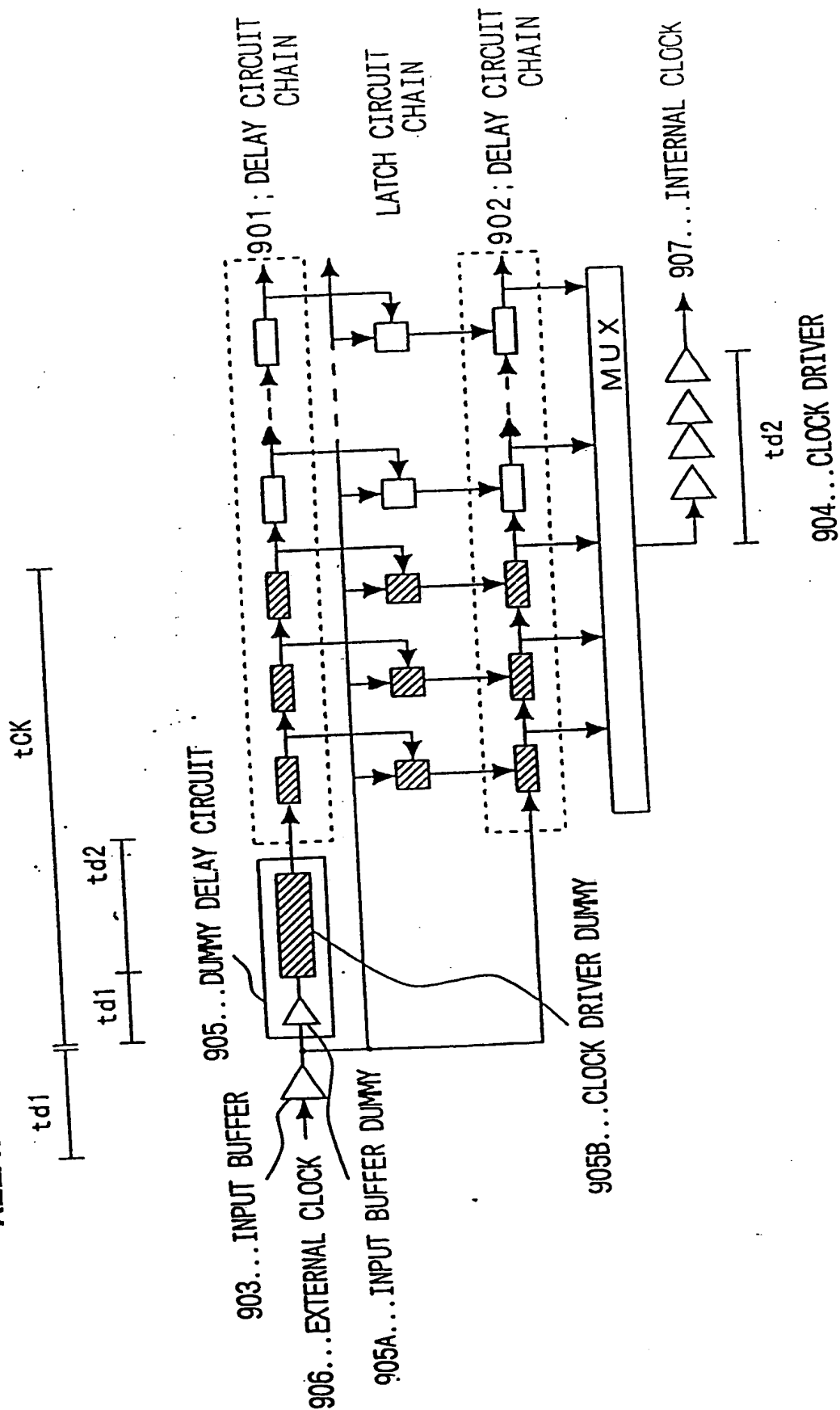


FIG. 27
RELATED ART

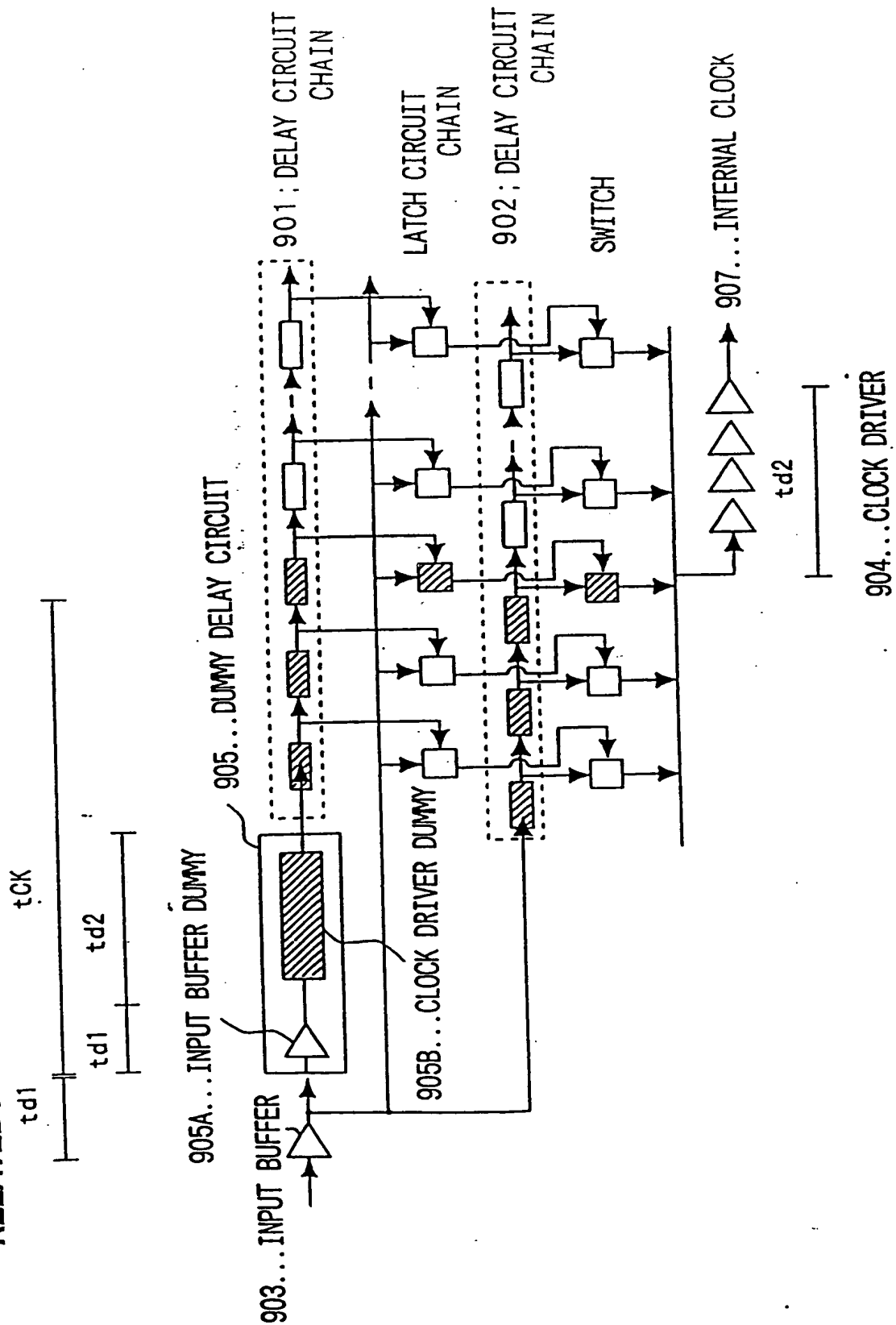


FIG. 28 (a)

RELATED ART

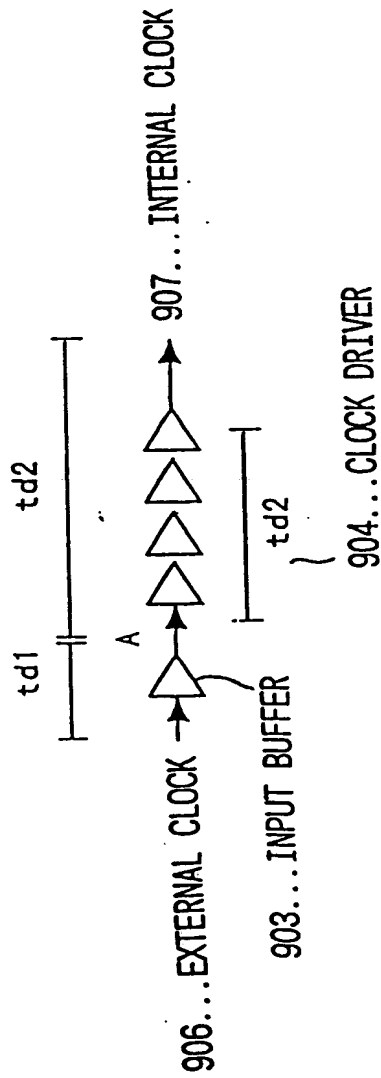


FIG. 28 (b)

RELATED ART

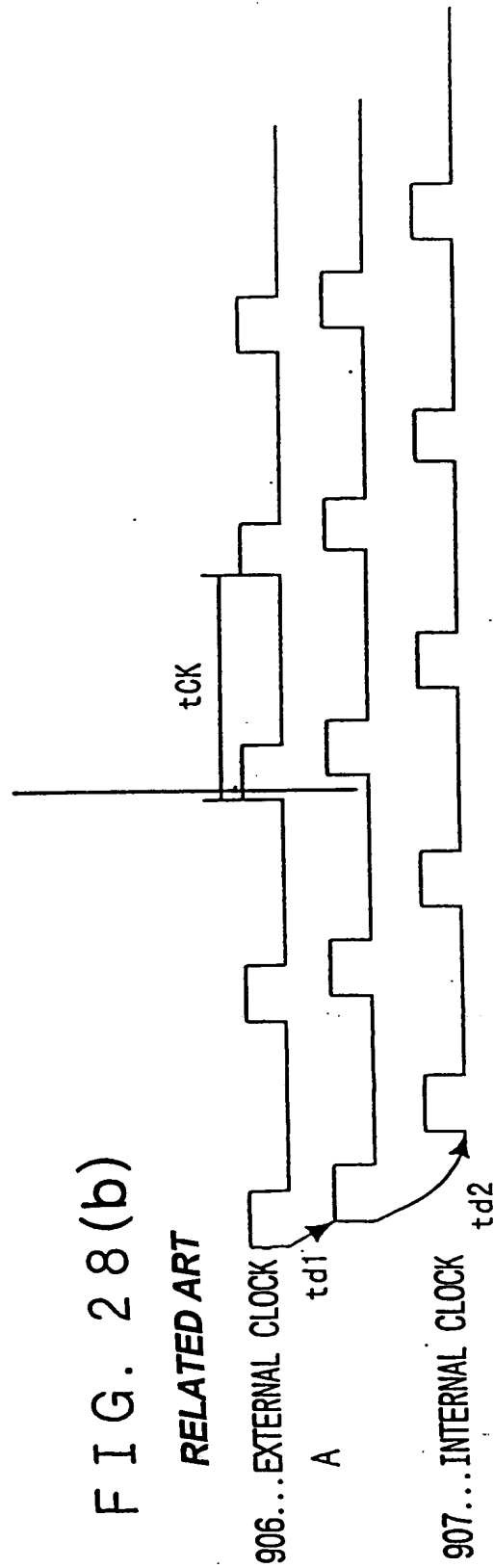


FIG. 29(a)

RELATED ART

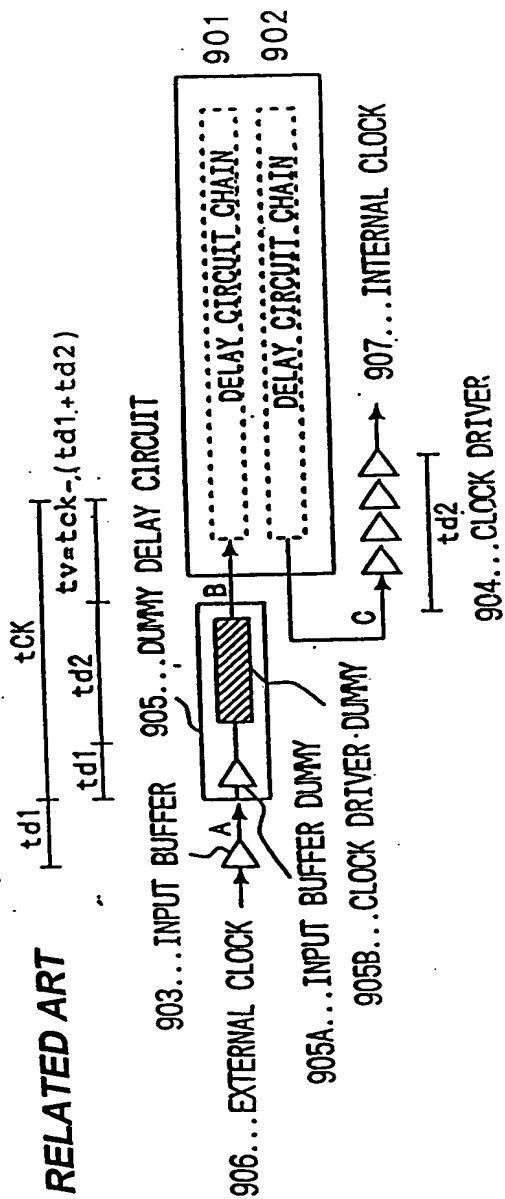


FIG. 29(b)

RELATED ART

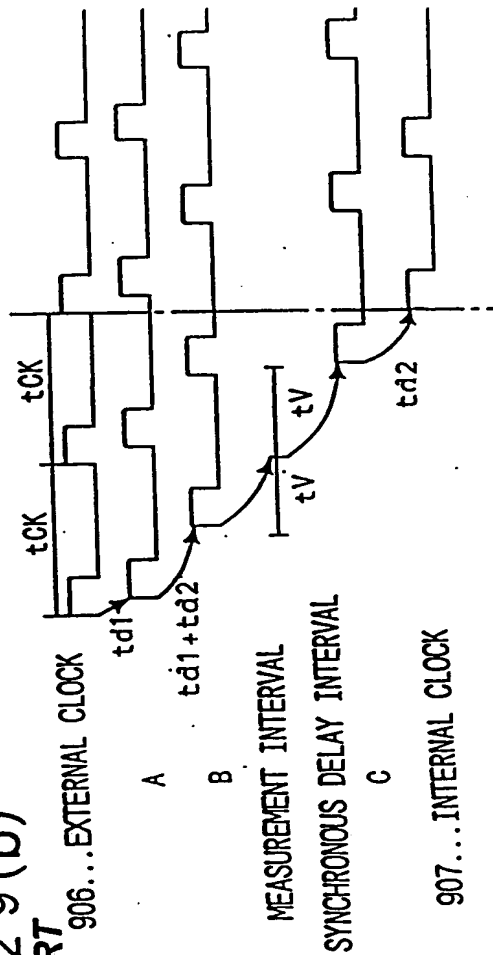


FIG. 30

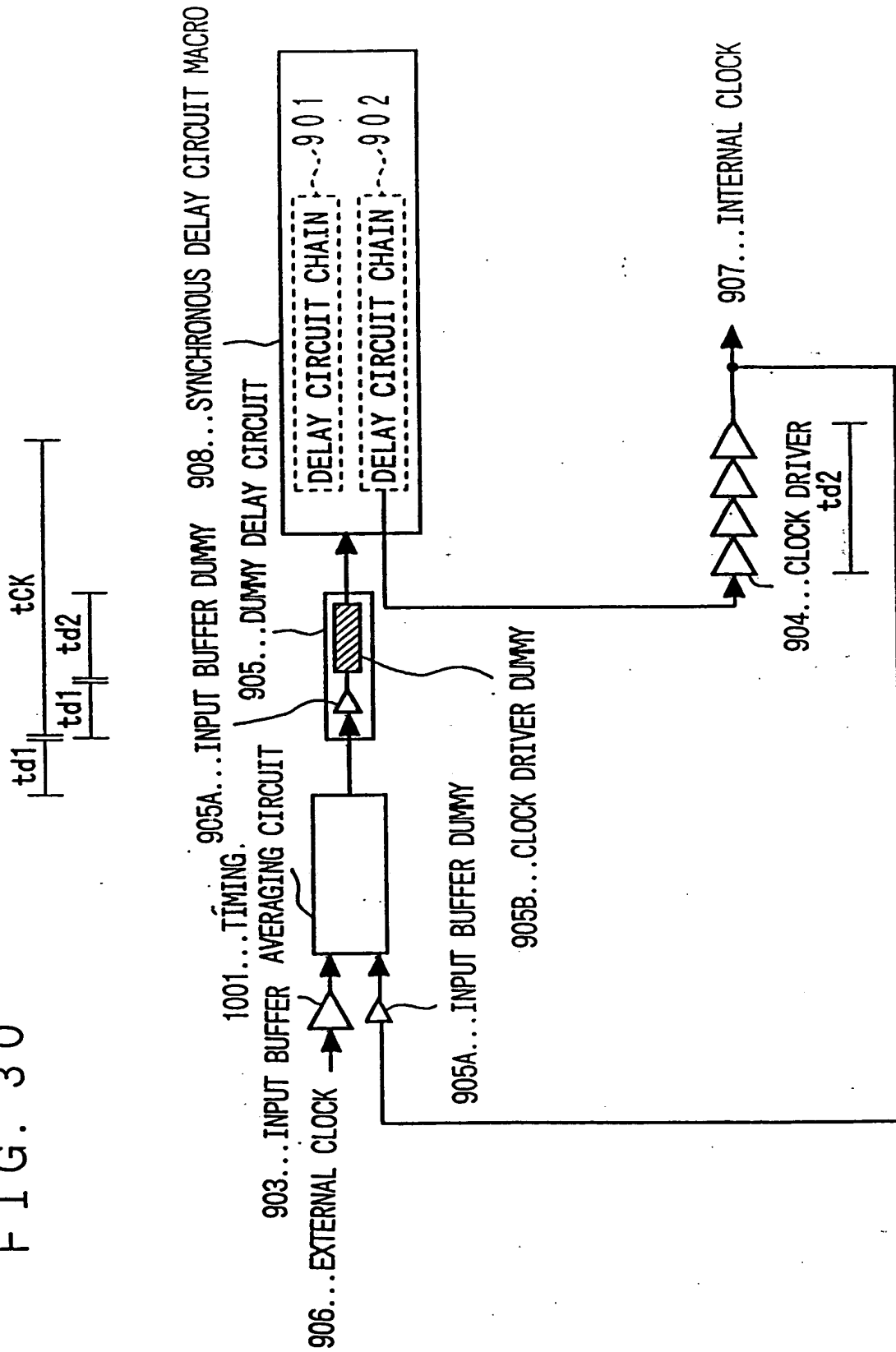


FIG. 31

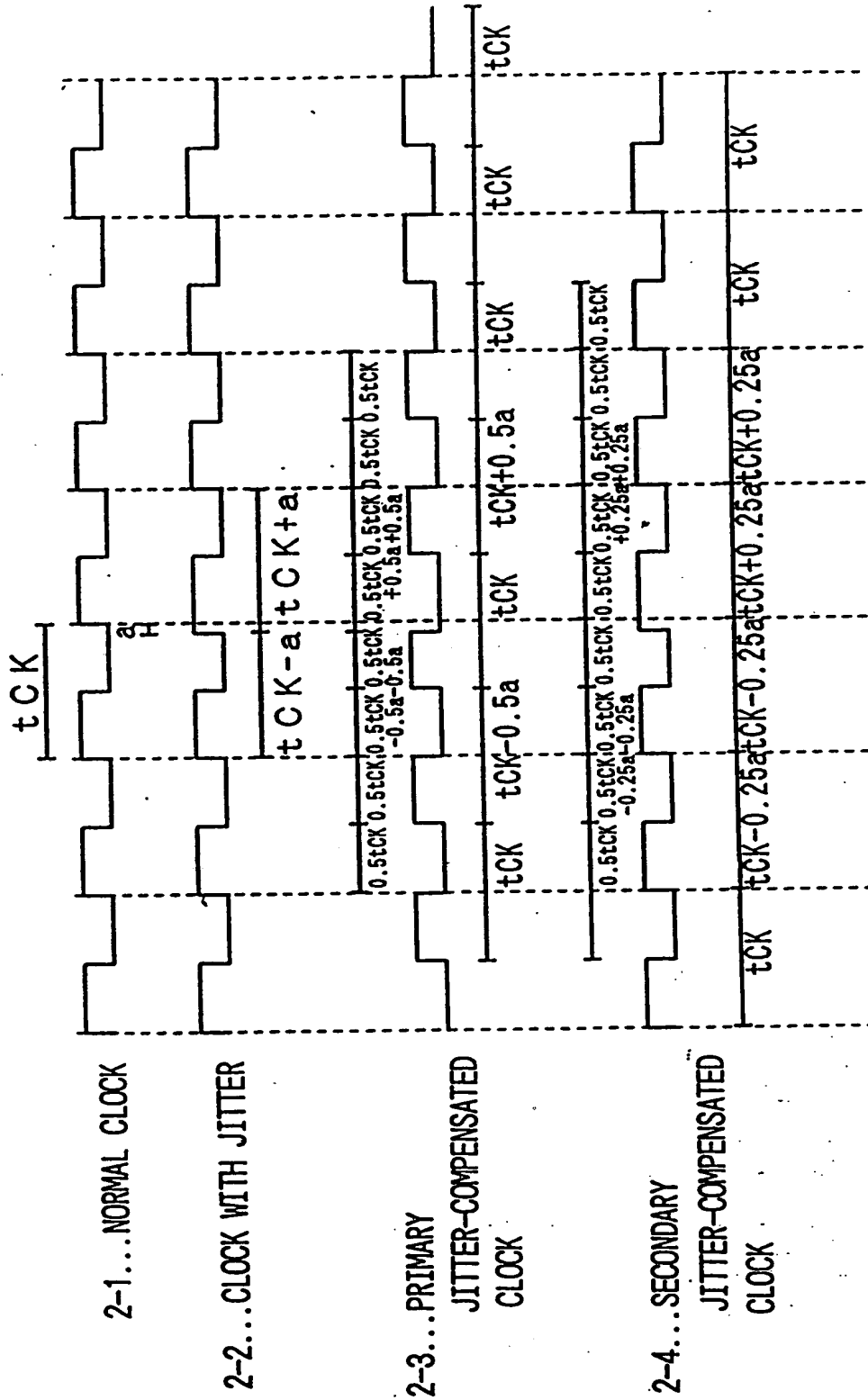


FIG. 32

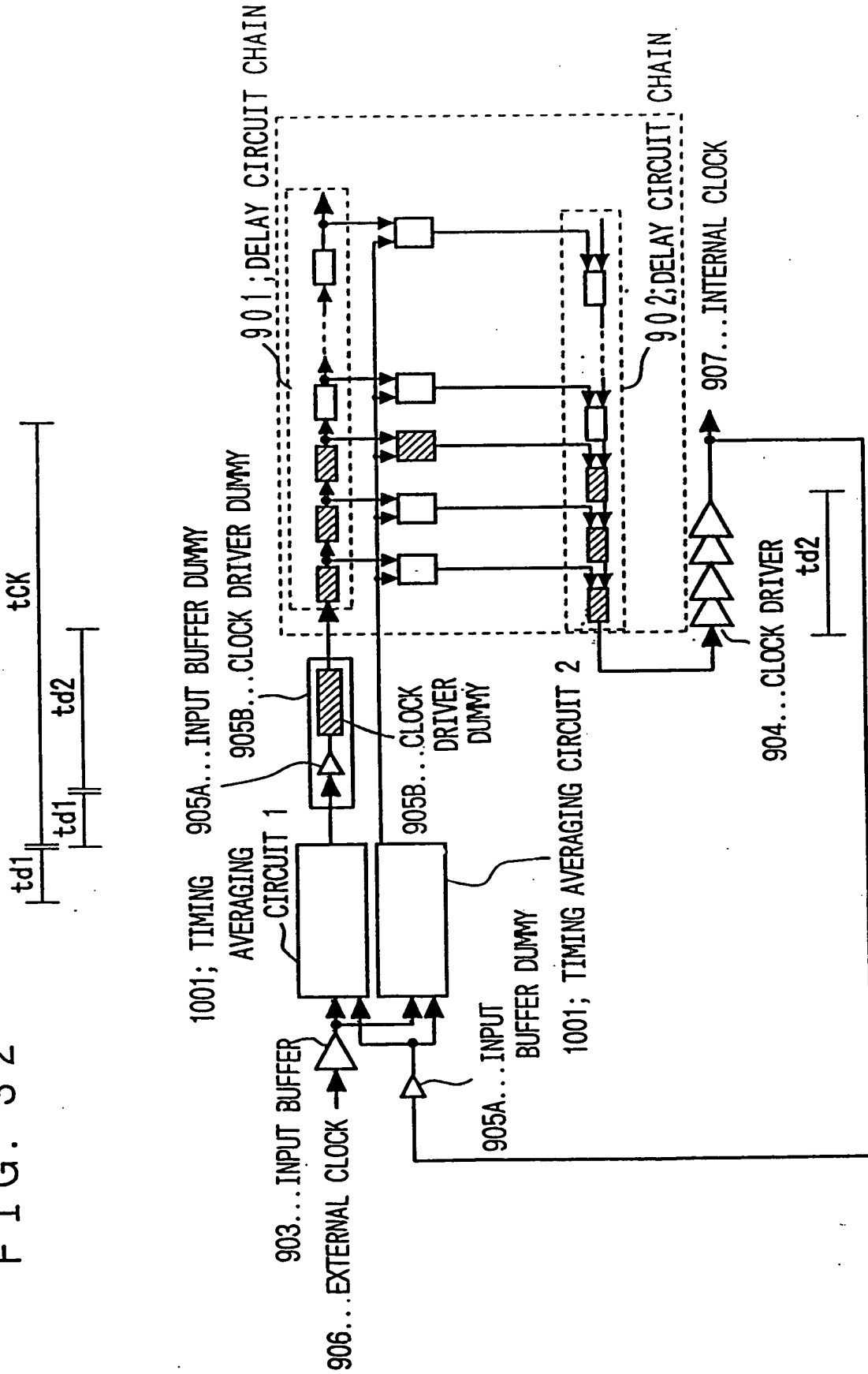


FIG. 34

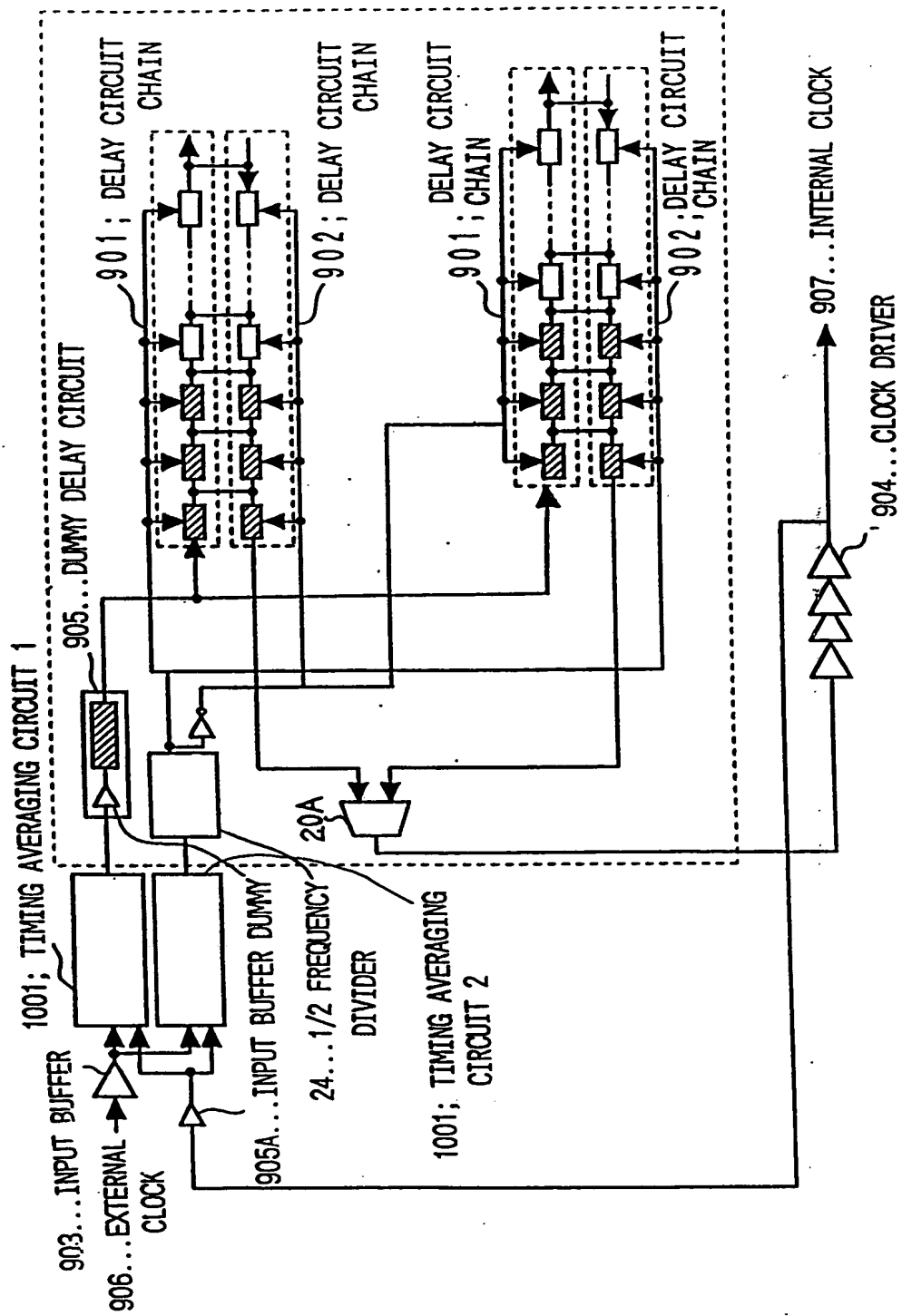


FIG. 35

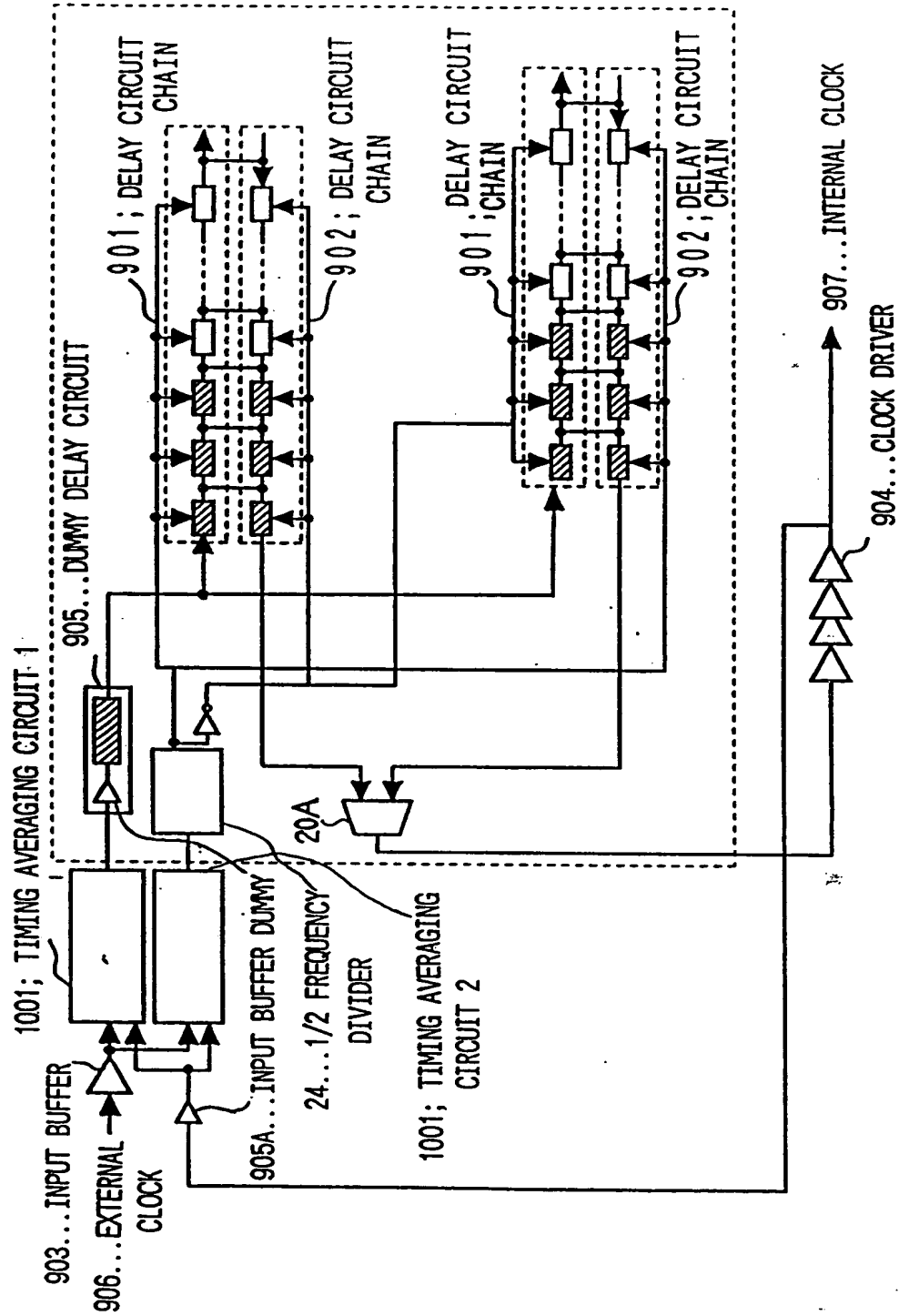
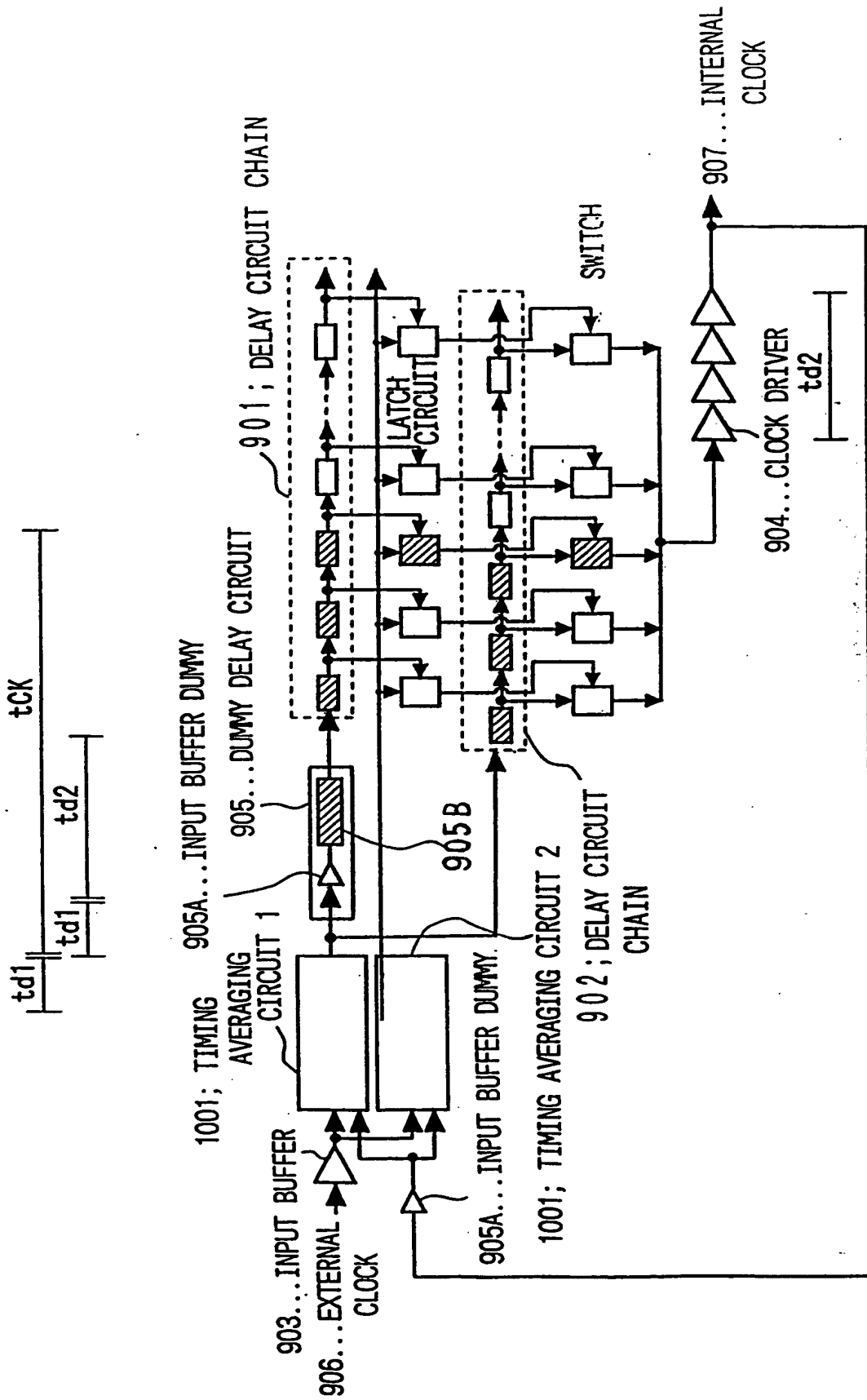


FIG. 36



1001; TIMING AVERAGING CIRCUIT

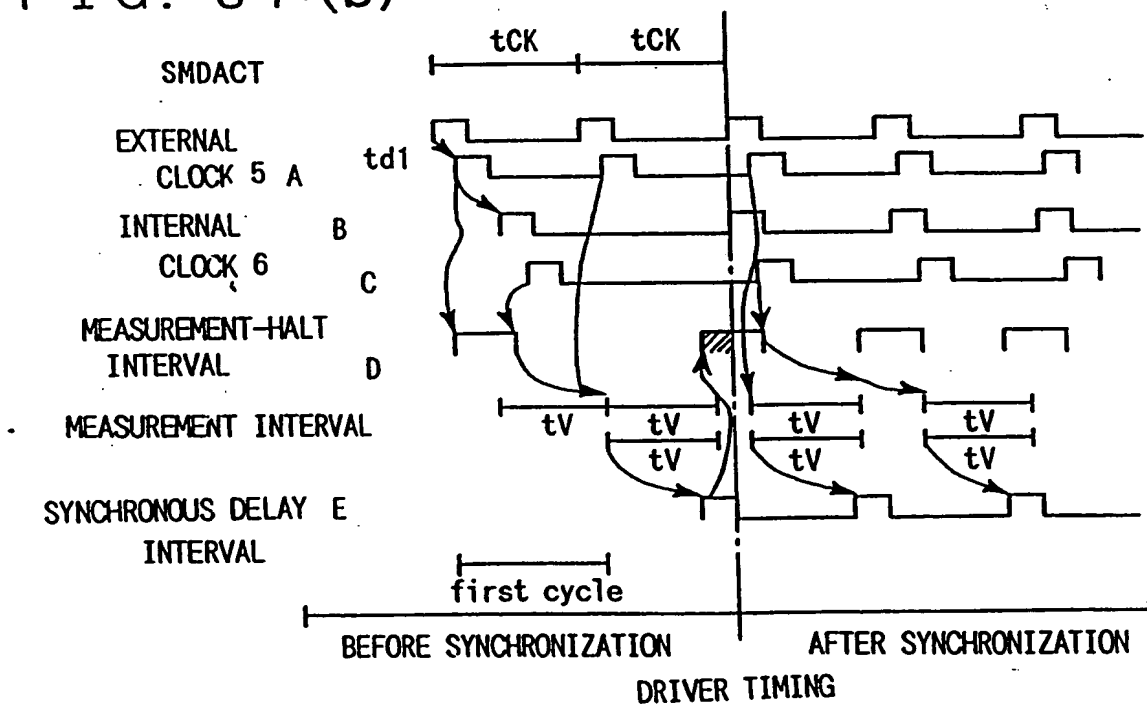


FIG. 38

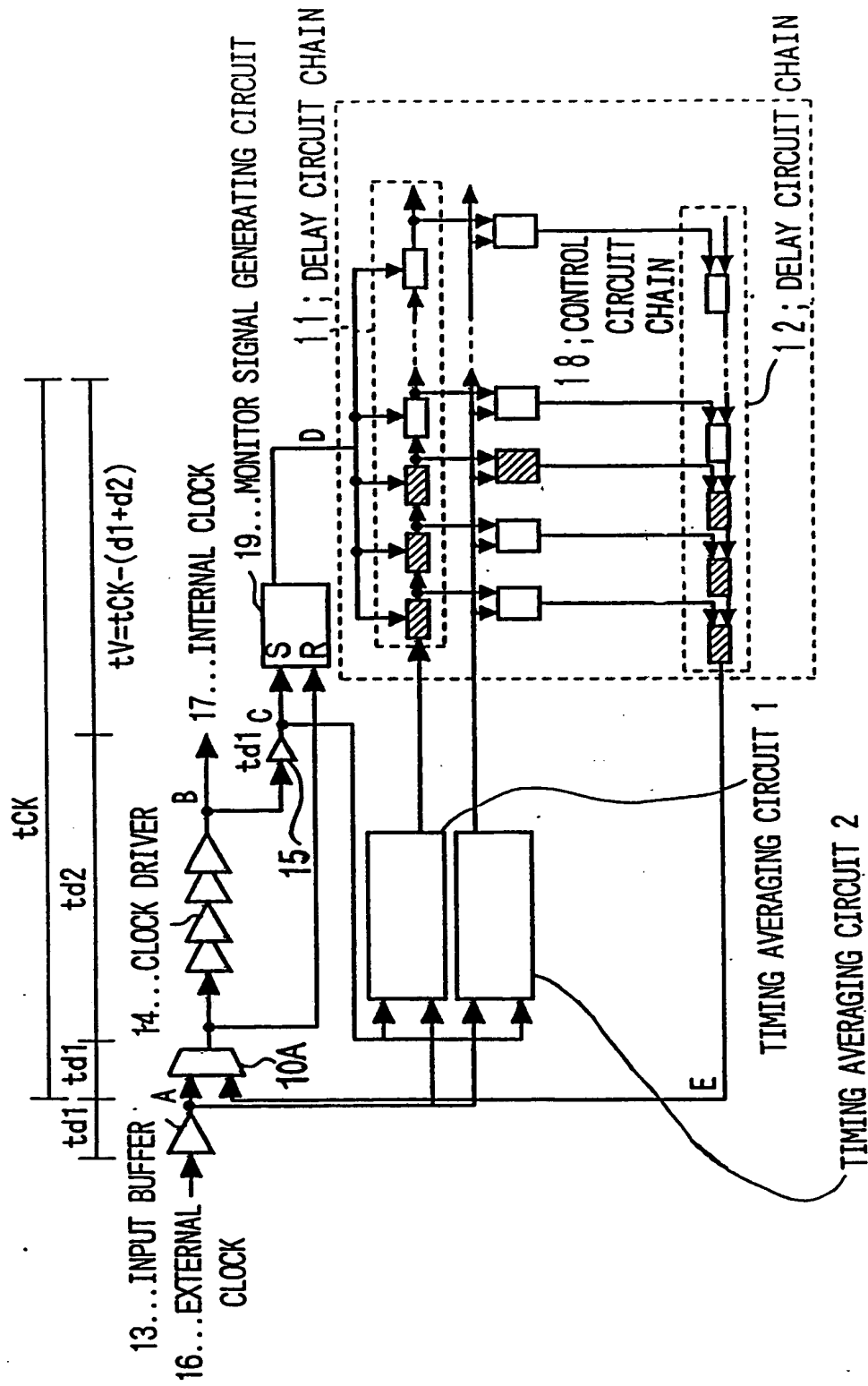


FIG. 39

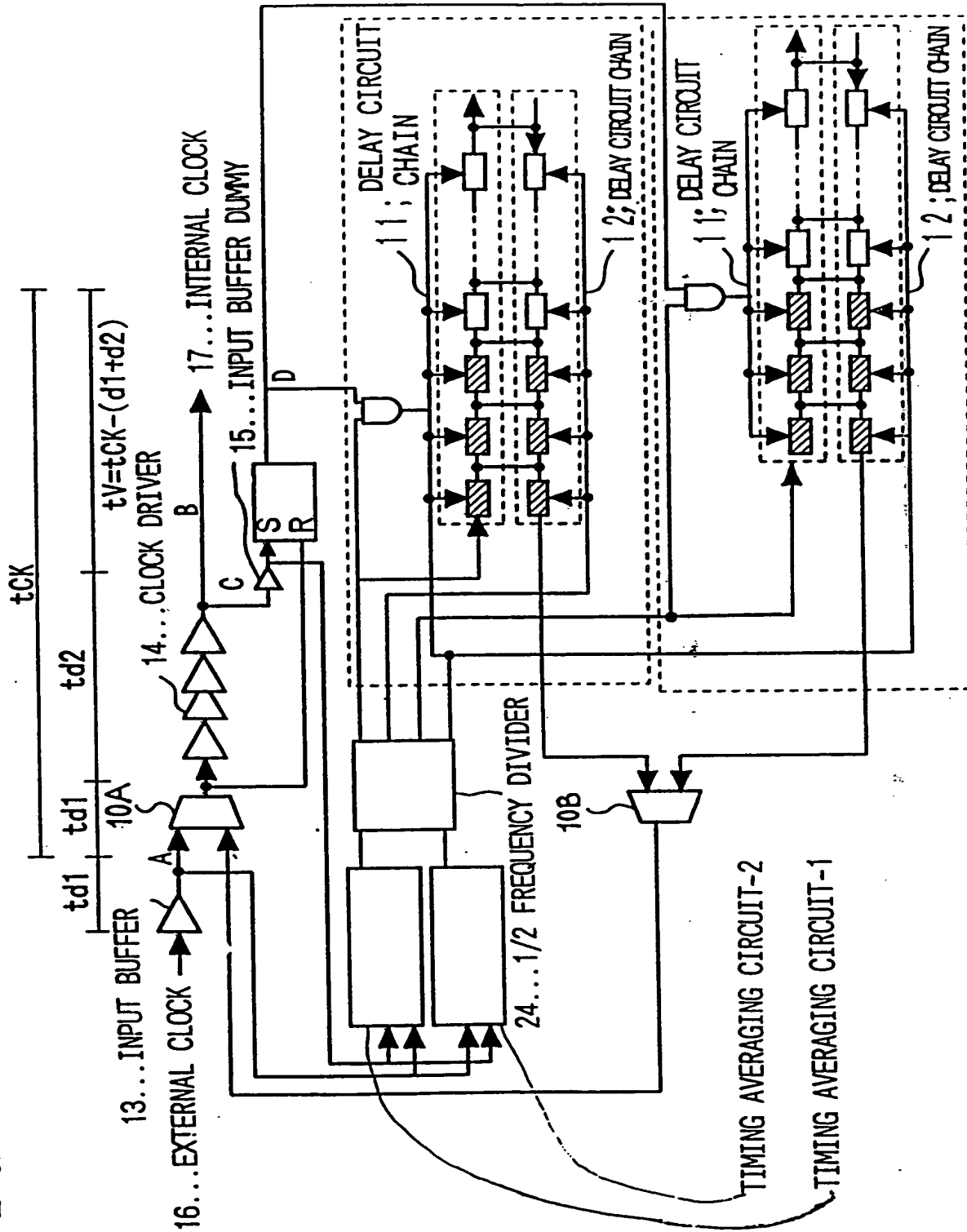


FIG. 40

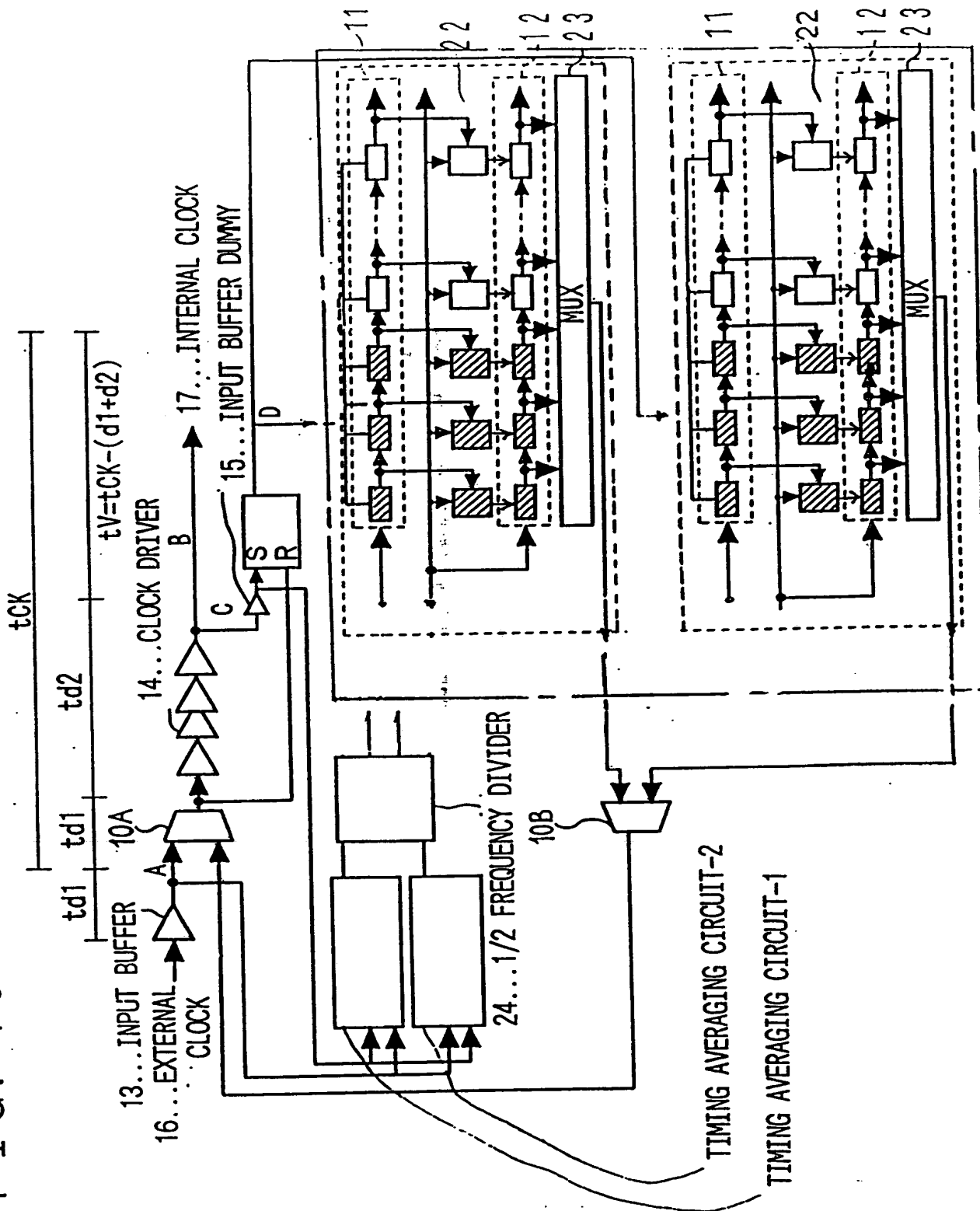


FIG. 41

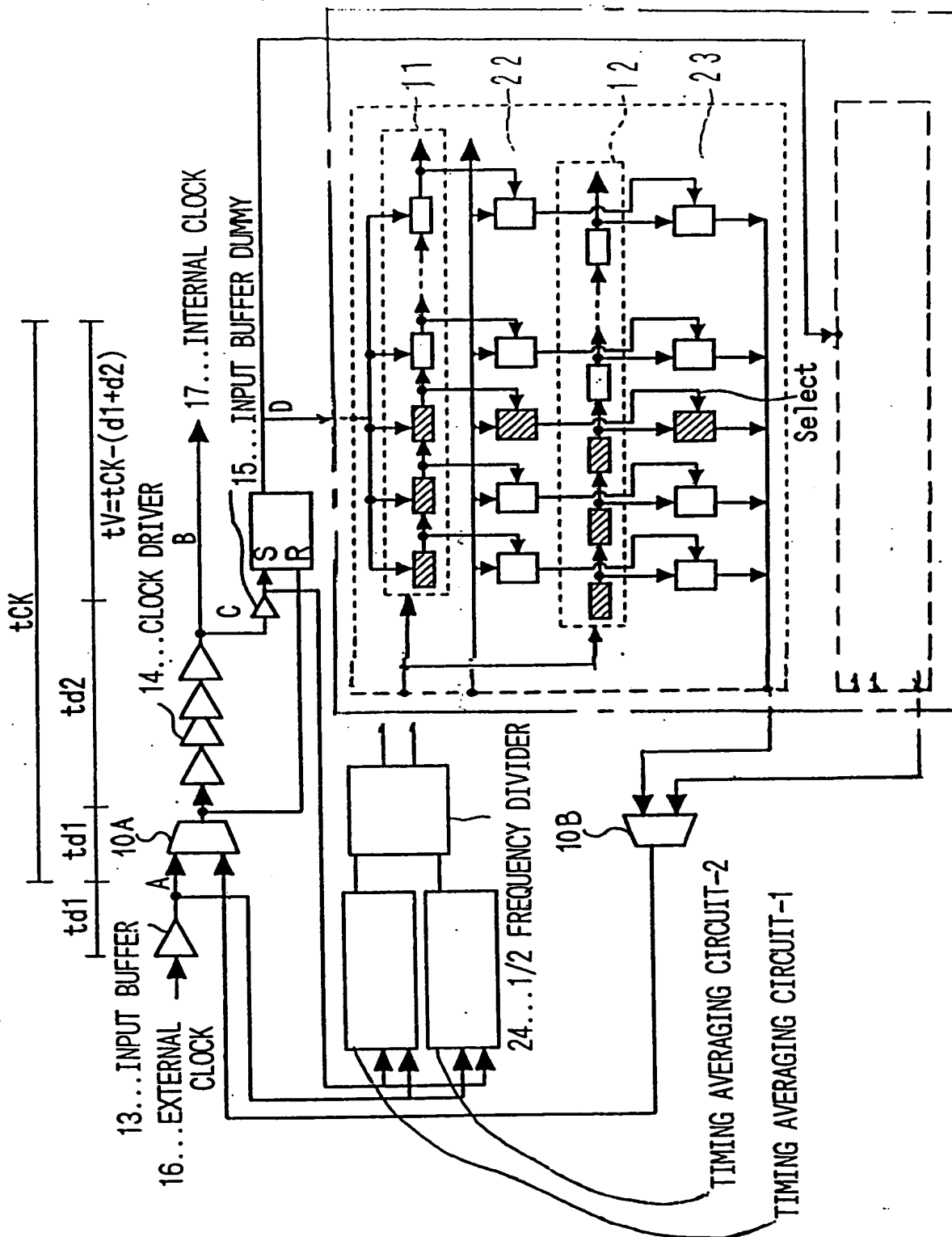


FIG. 42

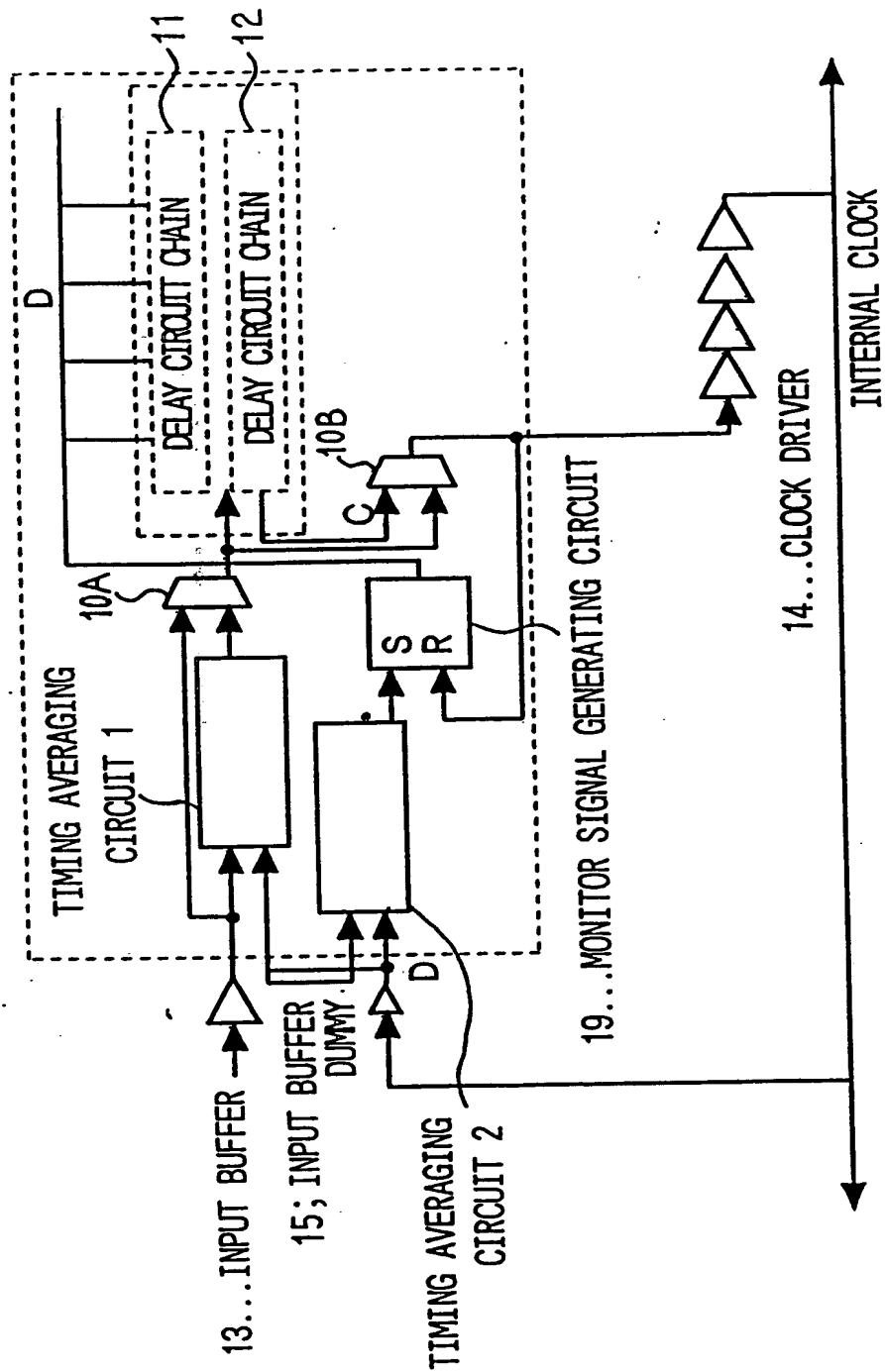


FIG. 44

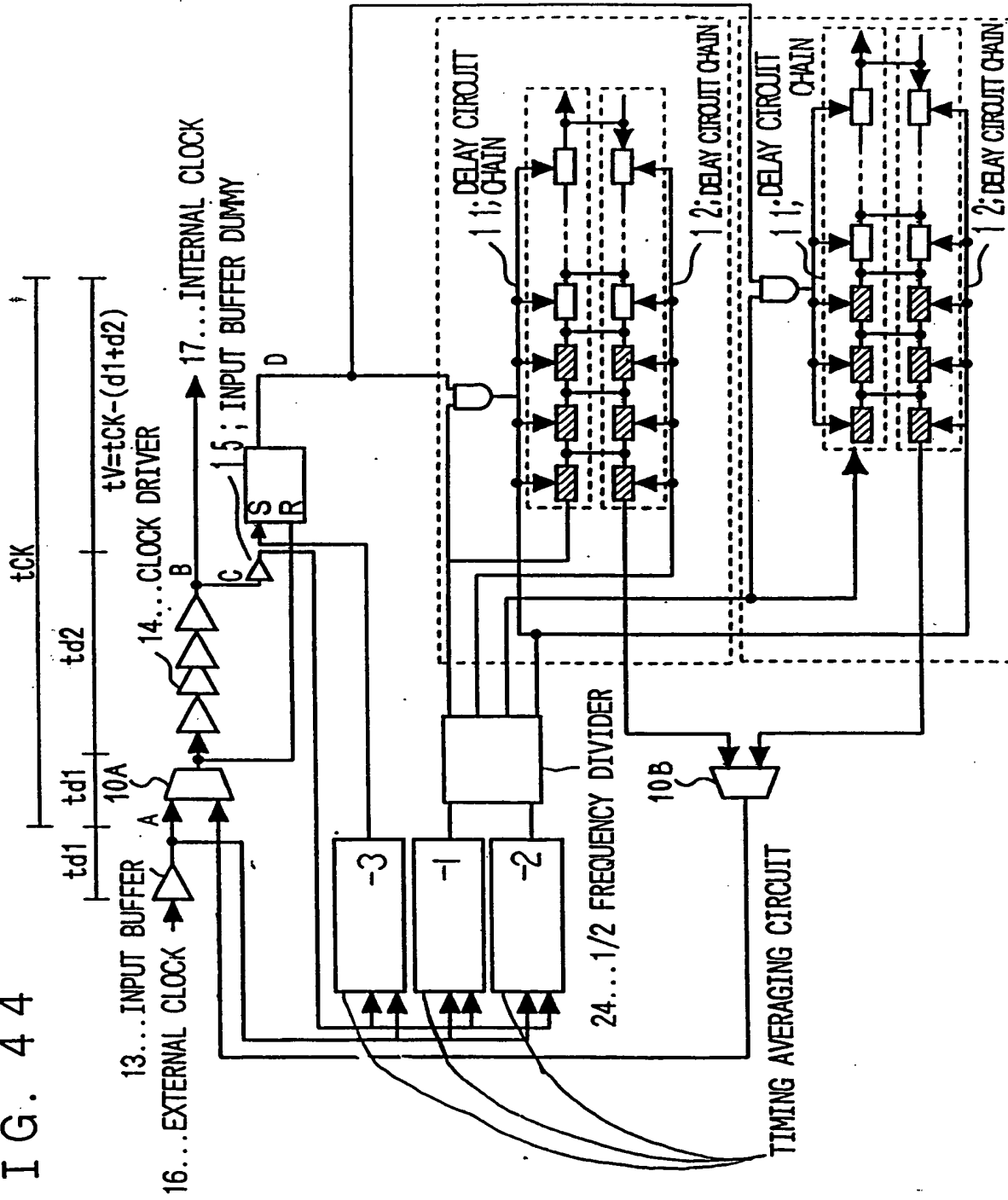


FIG. 45

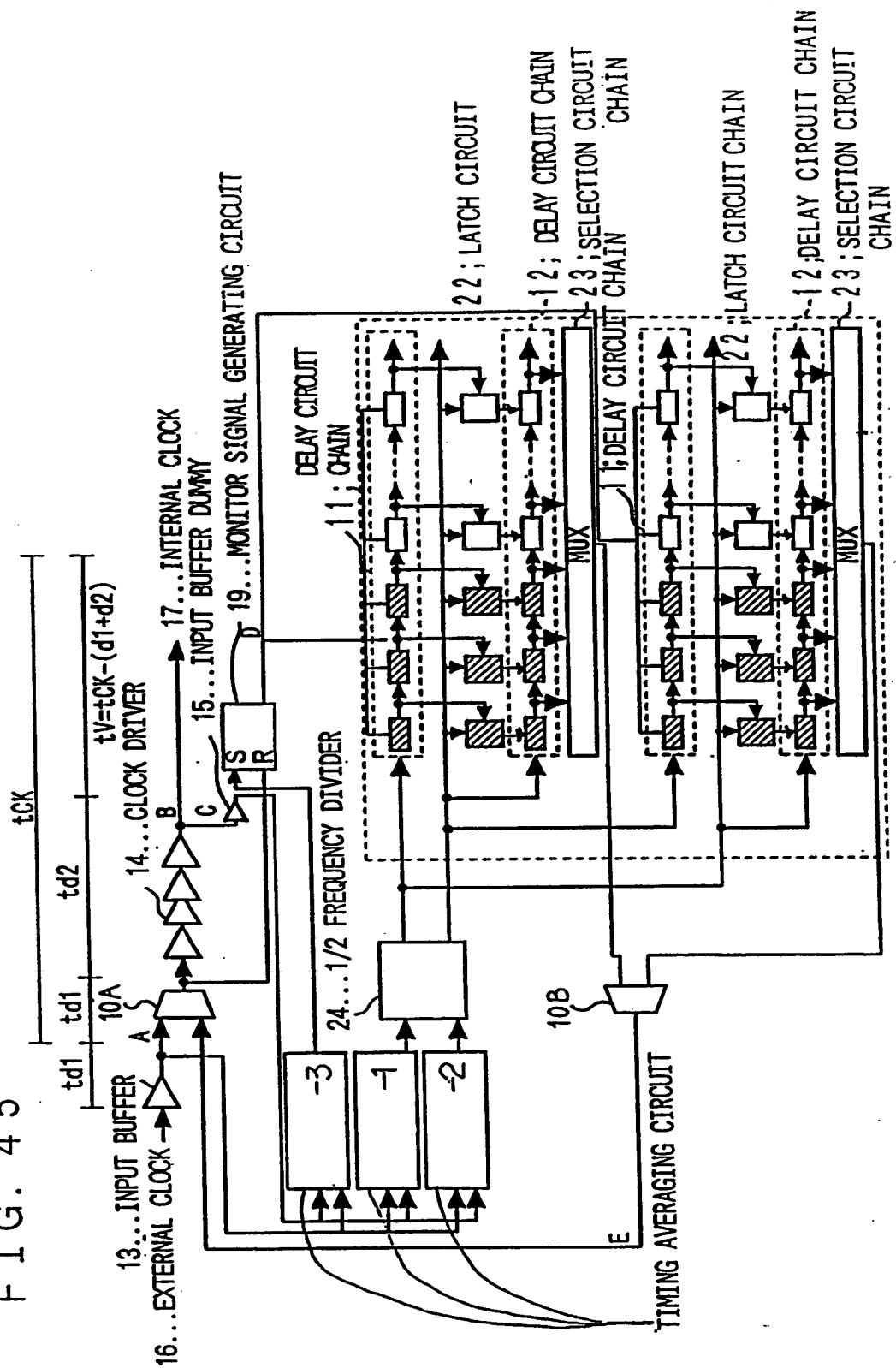


FIG. 46

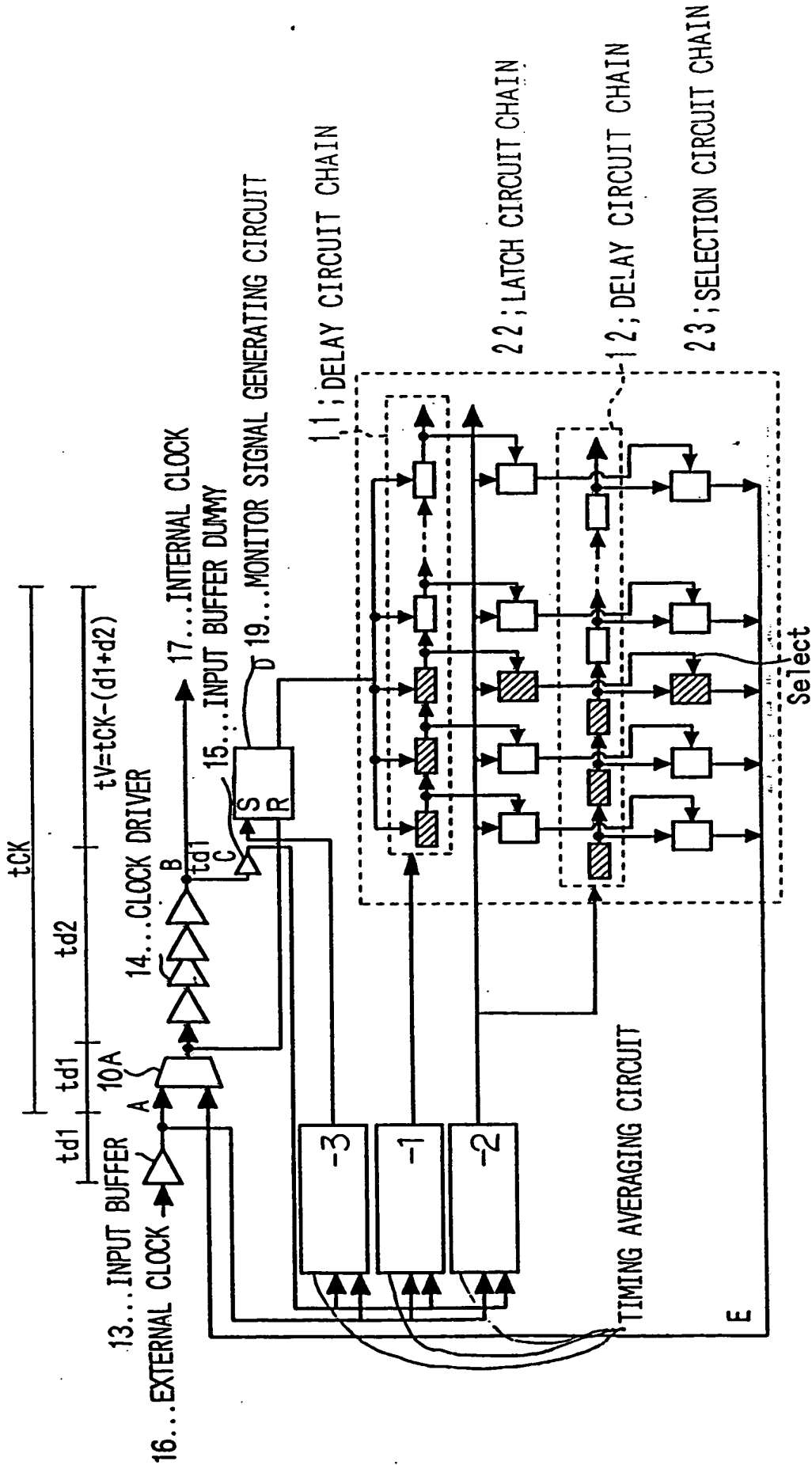
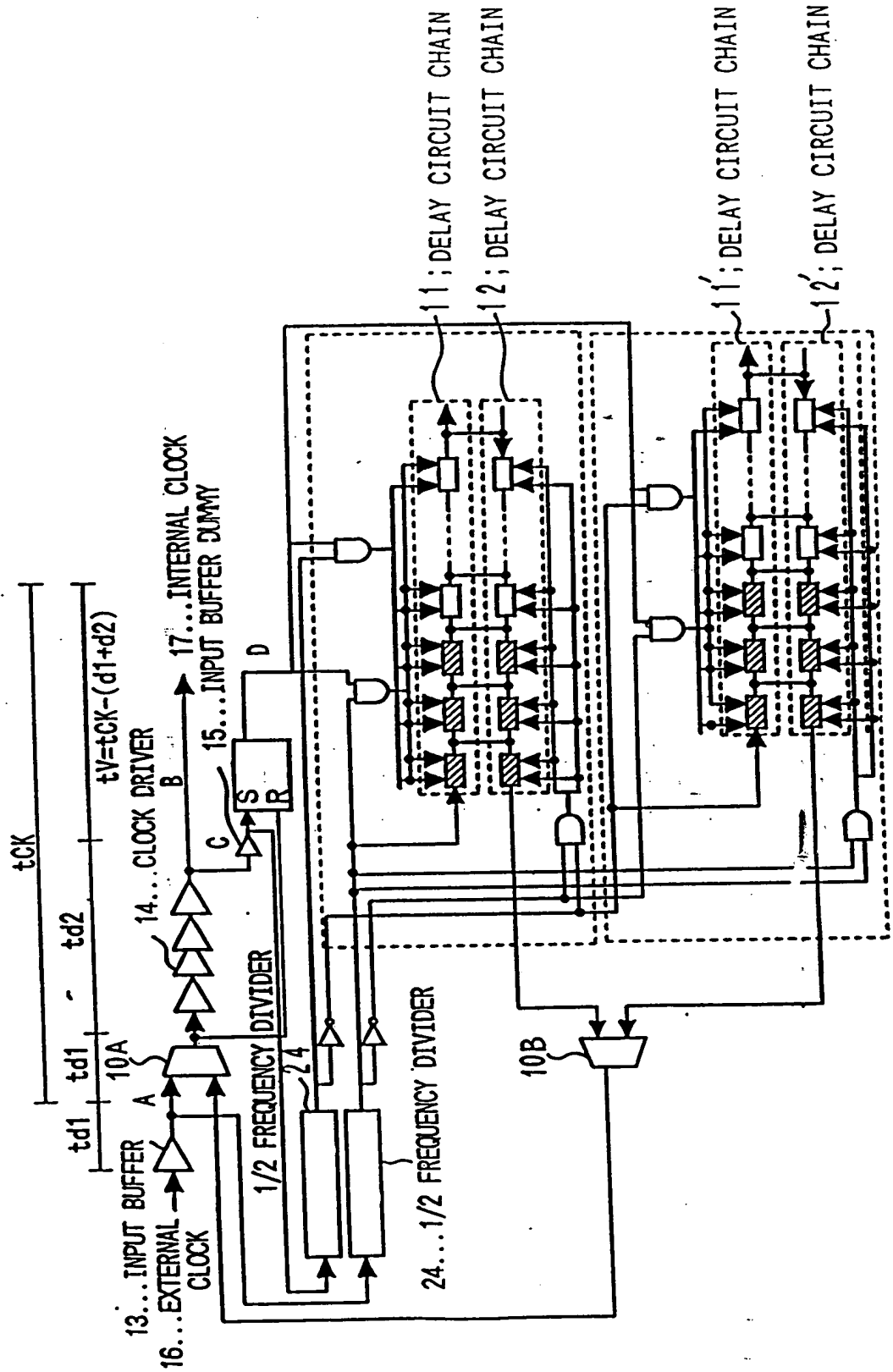


FIG. 47



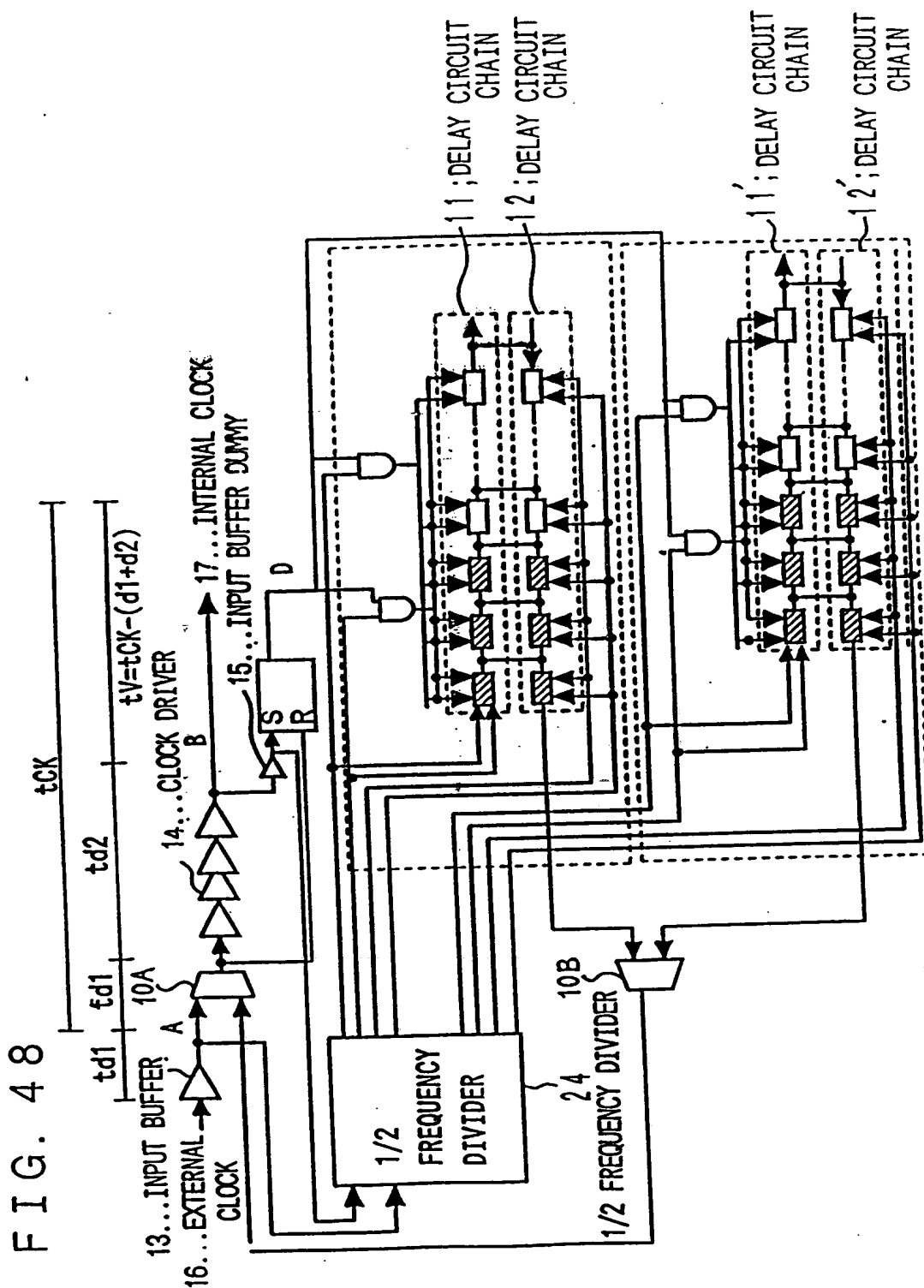


FIG. 49

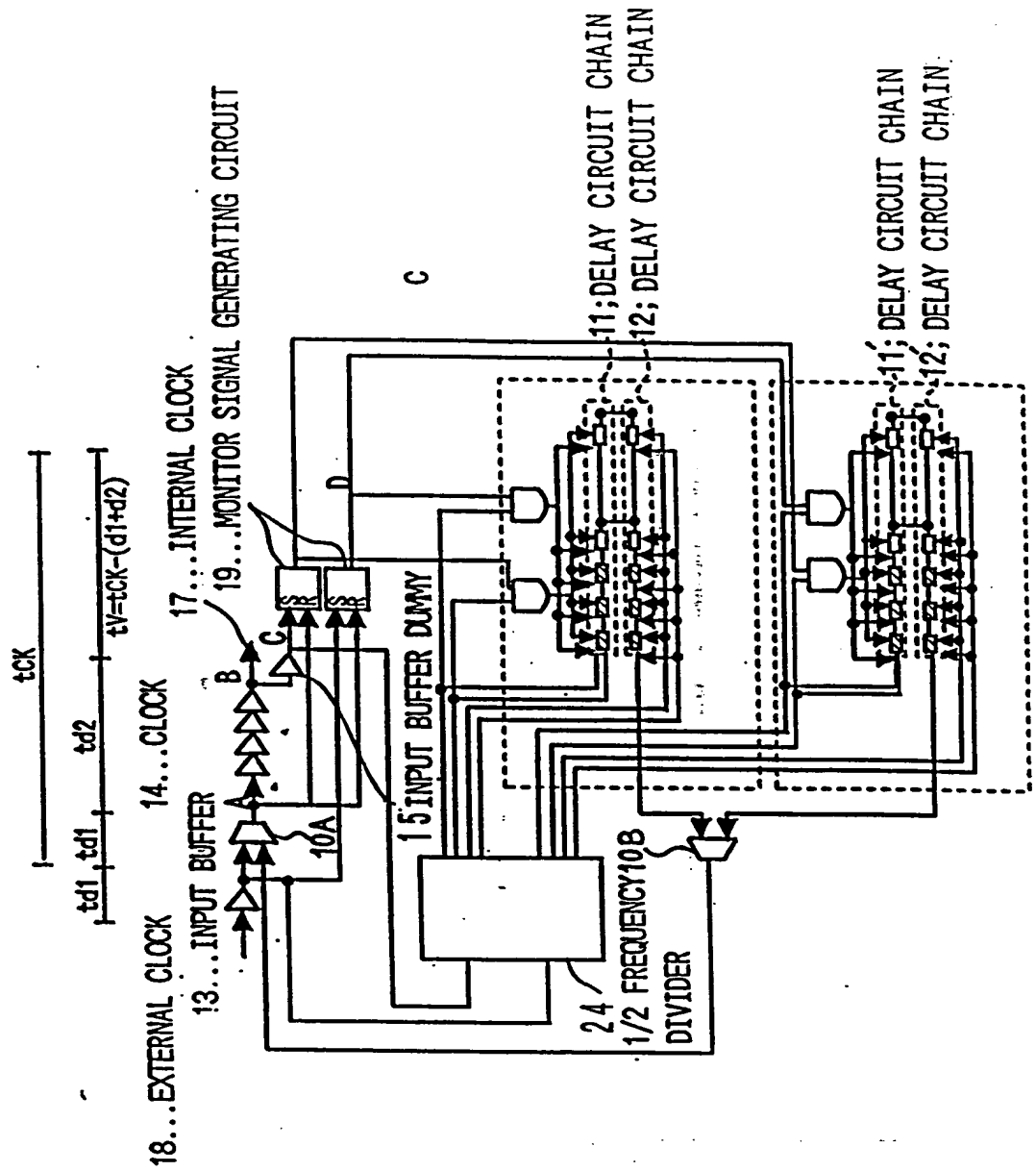


FIG. 51

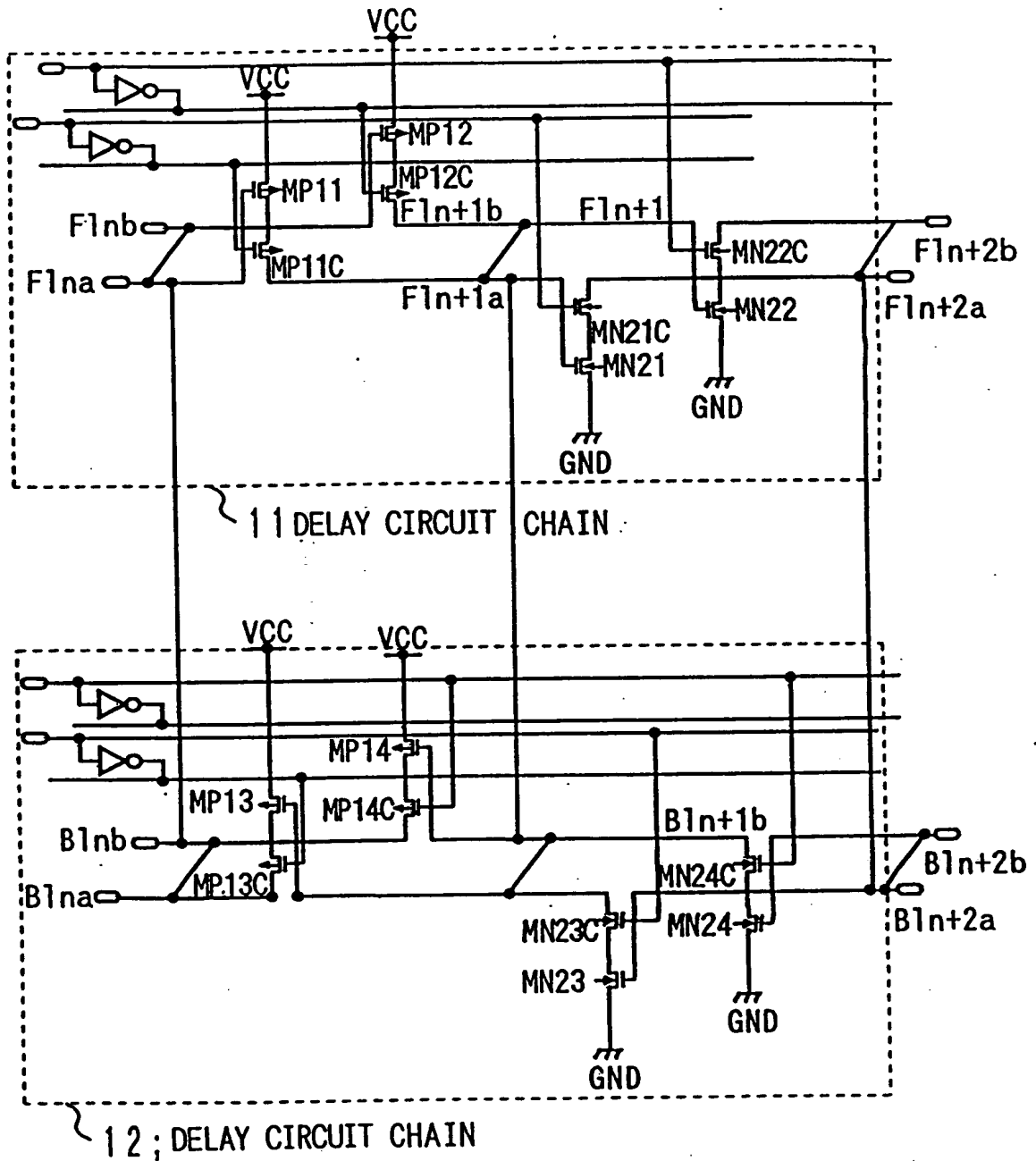


FIG. 52

